


BOM MARK

ED@	INT. VGA WITH DOCK
ID@	INT. VGA WITH DOCK
ND@	W/O DOCKING要打
E@	EXT VGA 要打
I@	INTVGA 要打
SA@	SATA 要打
F@	FIXED ODD要打
SW@	SWAPPABLE ODD 要打
3@	3in1 n打
NE@	NEW CARD 要打
4@	4401 n打
5@	5705M 要打
D@	DOCKING 要打

PM : 紀明進 Sunyu Jih  
EE Laerer : 劉鳴豹 Selmon Liu  
ME Leader : 林哲敏 Mill Lin

PCI ROUTING TABLE	IDSEL	INTERUPT	DEVICE
REQ0# / GNT0#	AD24	INTA#	BROADCOM LAN
REQ2# / GNT2#	AD19	INTB# , INTD#	MINI-PCI
REQ1# / GNT1#	AD17	INTC# , INTD# , INTA#	TI 7411



PROJECT : ZL2

Quanta Computer Inc.

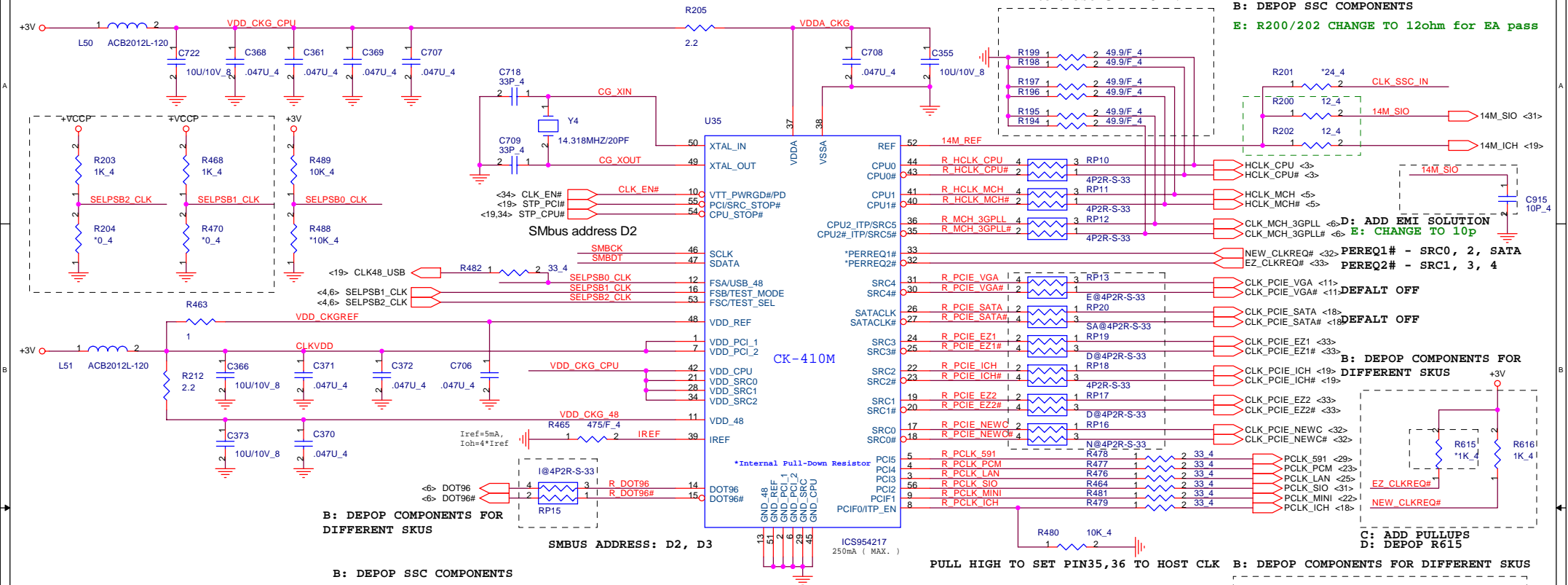
Size	Document Number	Rev
	BLOCK DIAGRAM	F
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REV B: POP R203 R468 AND DEPOP R204, R470 FOR DOTHAN B

Place these termination to close CK410M.

B: DEPOP SSC COMPONENTS

E: R200/202 CHANGE TO 12ohm for EA pass



FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

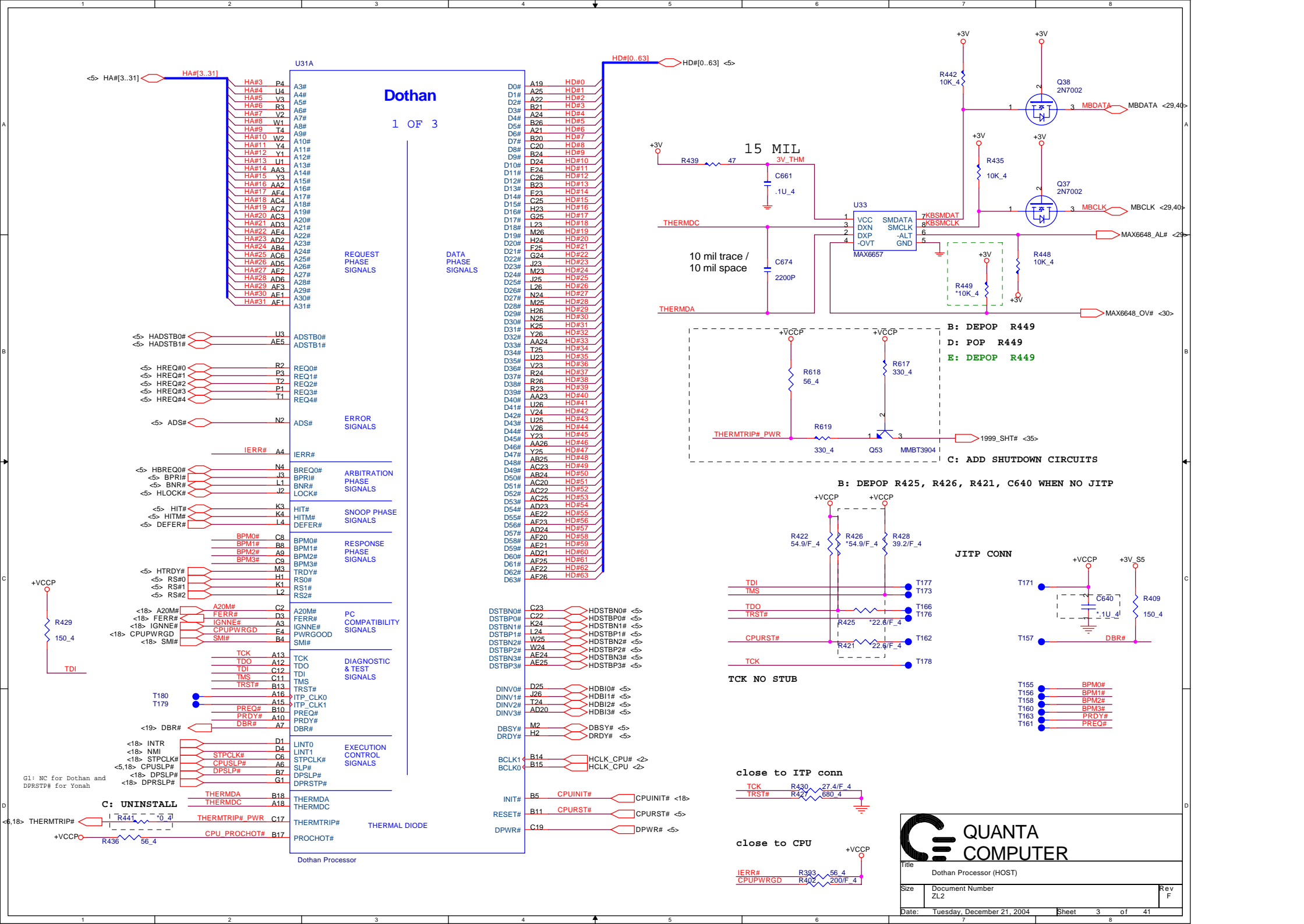
Place these termination to close CK410M.

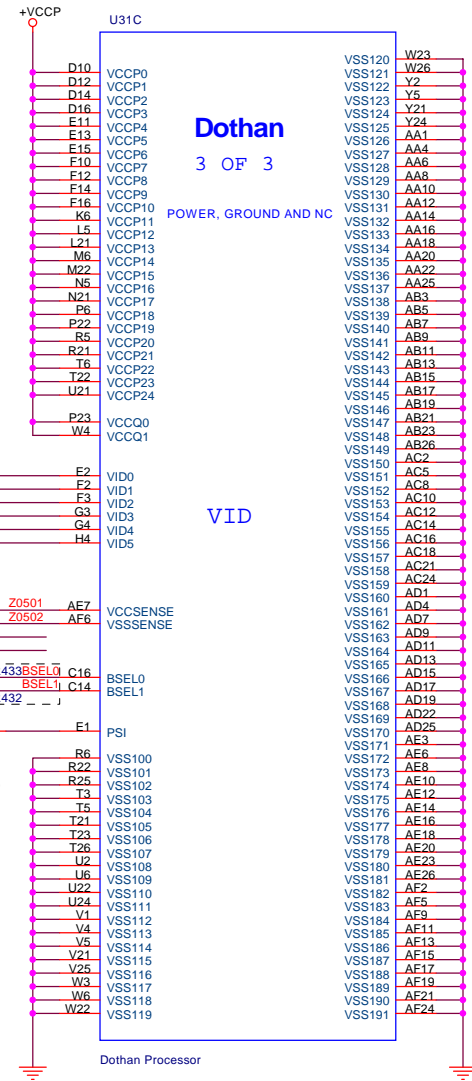
**QUANTA COMPUTER**

CLOCK GENERATOR

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WWW.AliSaler.Com

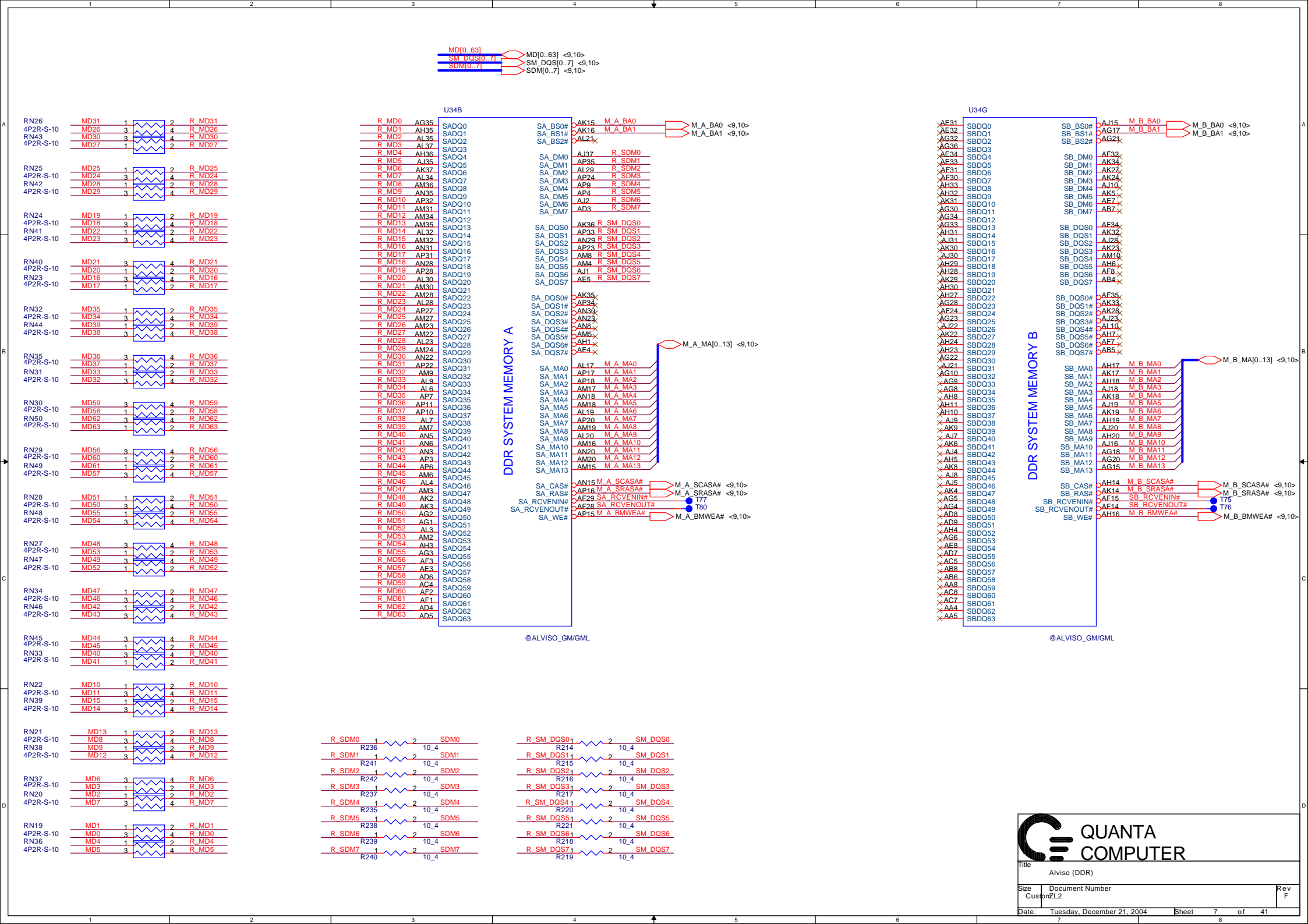




U34E		
AF23	VSS136	AG37
H23	VSS137	Y37
AL22	VSS138	V37
AH22	VSS139	T37
J22	VSS140	P37
E22	VSS141	M37
D22	VSS142	K37
A22	VSS143	H37
AN21	VSS144	E37
AF21	VSS145	AN36
F21	VSS146	AL36
C21	VSS147	AJ36
AK20	VSS148	AF36
V20	VSS149	AE36
G20	VSS150	AD36
F20	VSS151	AC36
E20	VSS152	AB36
D20	VSS153	AA36
AD20	VSS154	CA36
AN19	VSS155	AE35
AG19	VSS156	Y35
W19	VSS157	W35
T19	VSS158	V35
H19	VSS159	T35
V19	VSS160	P35
C19	VSS161	R35
AL18	VSS162	M35
U18	VSS163	N35
B18	VSS164	V35
AN17	VSS165	K35
AF17	VSS166	L35
G17	VSS167	F35
C17	VSS168	D35
AL16	VSS169	B35
K16	VSS170	AN34
D16	VSS171	AH34
H16	VSS172	AD34
D16	VSS173	AC34
A16	VSS174	AB34
K15	VSS175	AA34
C15	VSS176	CA34
AN14	VSS177	AE34
AL14	VSS178	Y34
AJ14	VSS179	W34
AG14	VSS180	V34
V14	VSS181	T34
K14	VSS182	P34
B14	VSS183	R34
A14	VSS184	M34
D14	VSS185	N34
J12	VSS186	V34
B12	VSS187	K34
D12	VSS188	L34
AN11	VSS189	F34
AL11	VSS190	D34
AF11	VSS191	B34
AA11	VSS192	AN33
Y11	VSS193	AH33
H11	VSS194	AD33
F11	VSS195	AC33
AA10	VSS196	AB33
Y10	VSS197	AA33
L10	VSS198	CA33
D10	VSS199	AE33
AN9	VSS200	Y33
AH9	VSS201	W33
AE9	VSS202	V33
AC9	VSS203	T33
AA9	VSS204	P33
Y9	VSS205	R33
T9	VSS206	M33
K9	VSS207	N33
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AL8	VSS210	L33
Y8	VSS211	F33
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AG7	VSS217	AC32
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AA6	VSS223	W32
T6	VSS224	V32
PR6	VSS225	T32
L6	VSS226	P32
BE6	VSS227	R32
AP5	VSS228	M32
AL5	VSS229	N32
W5	VSS230	V32
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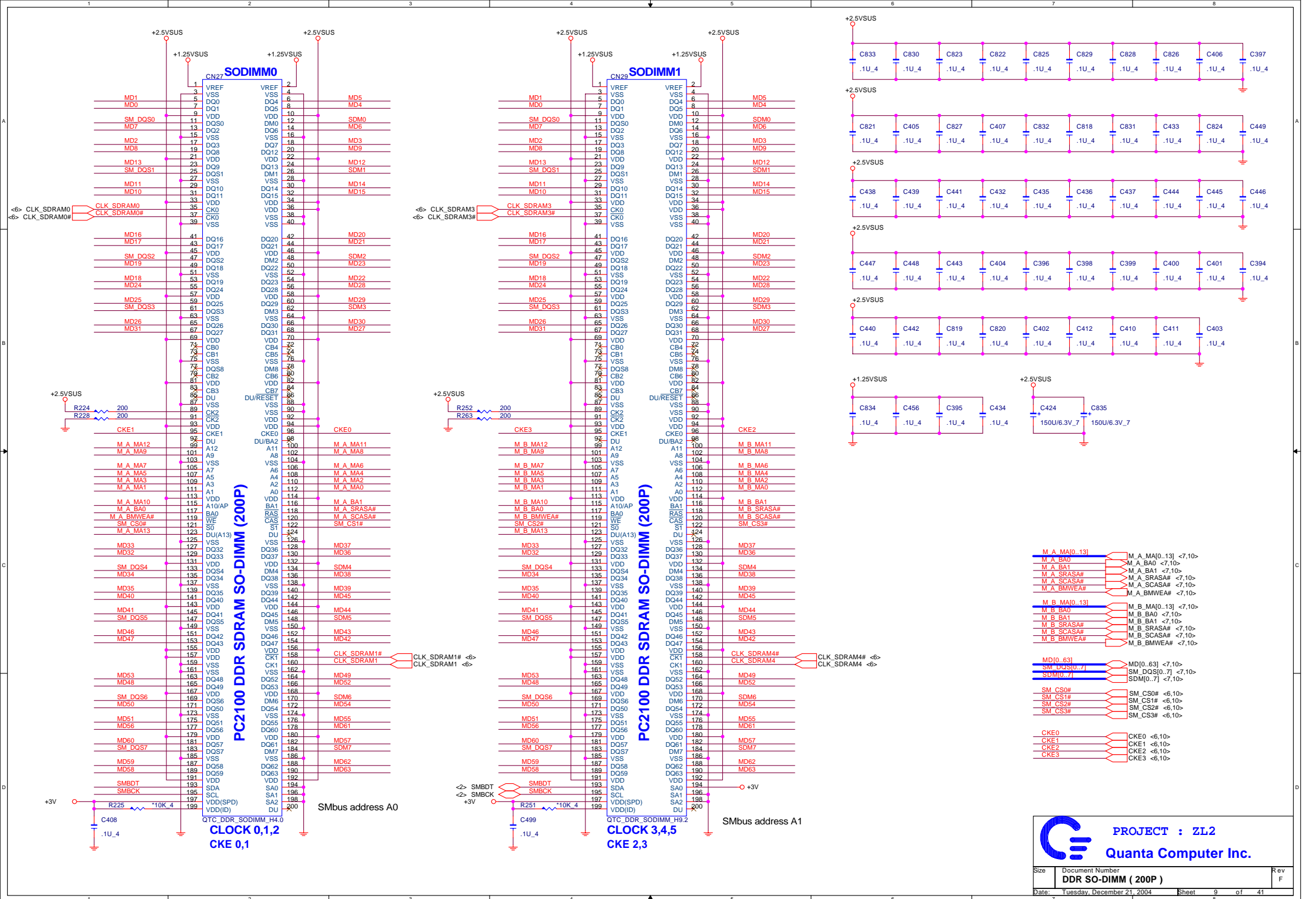








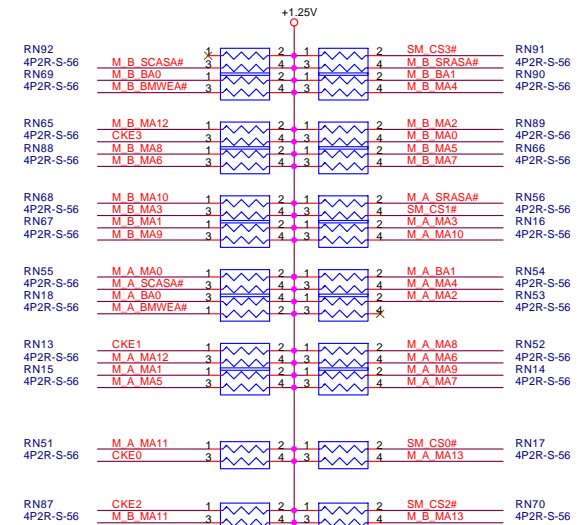
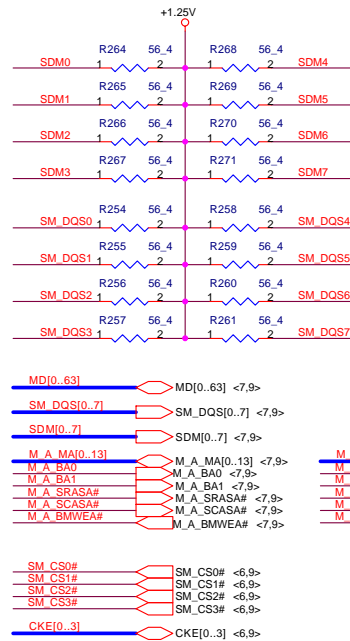
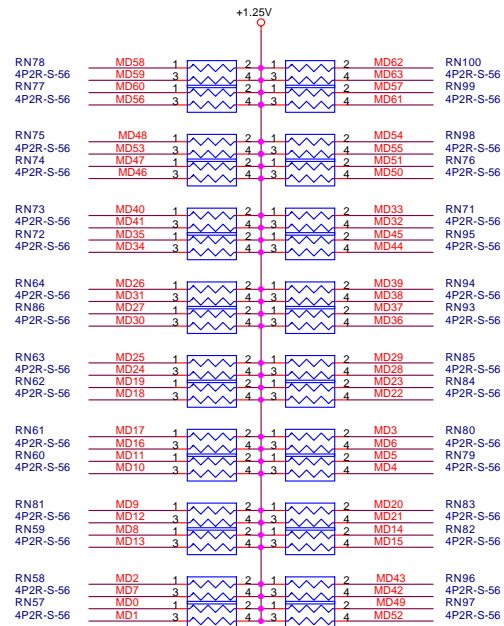
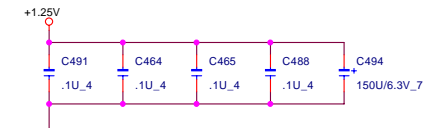
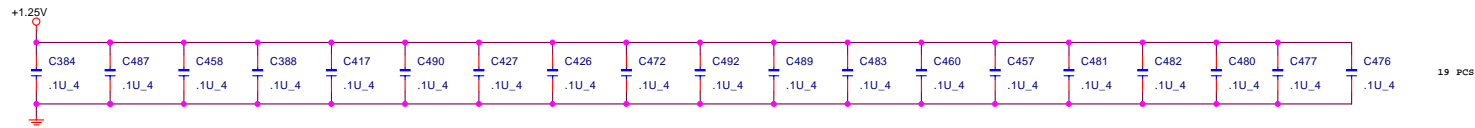
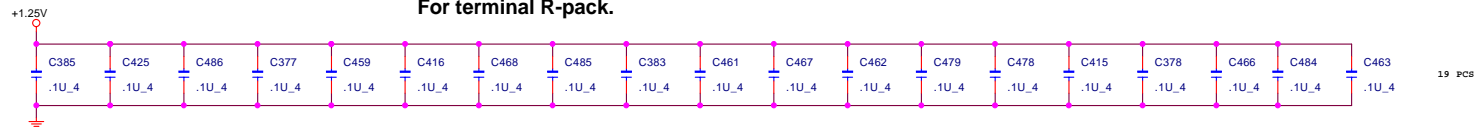


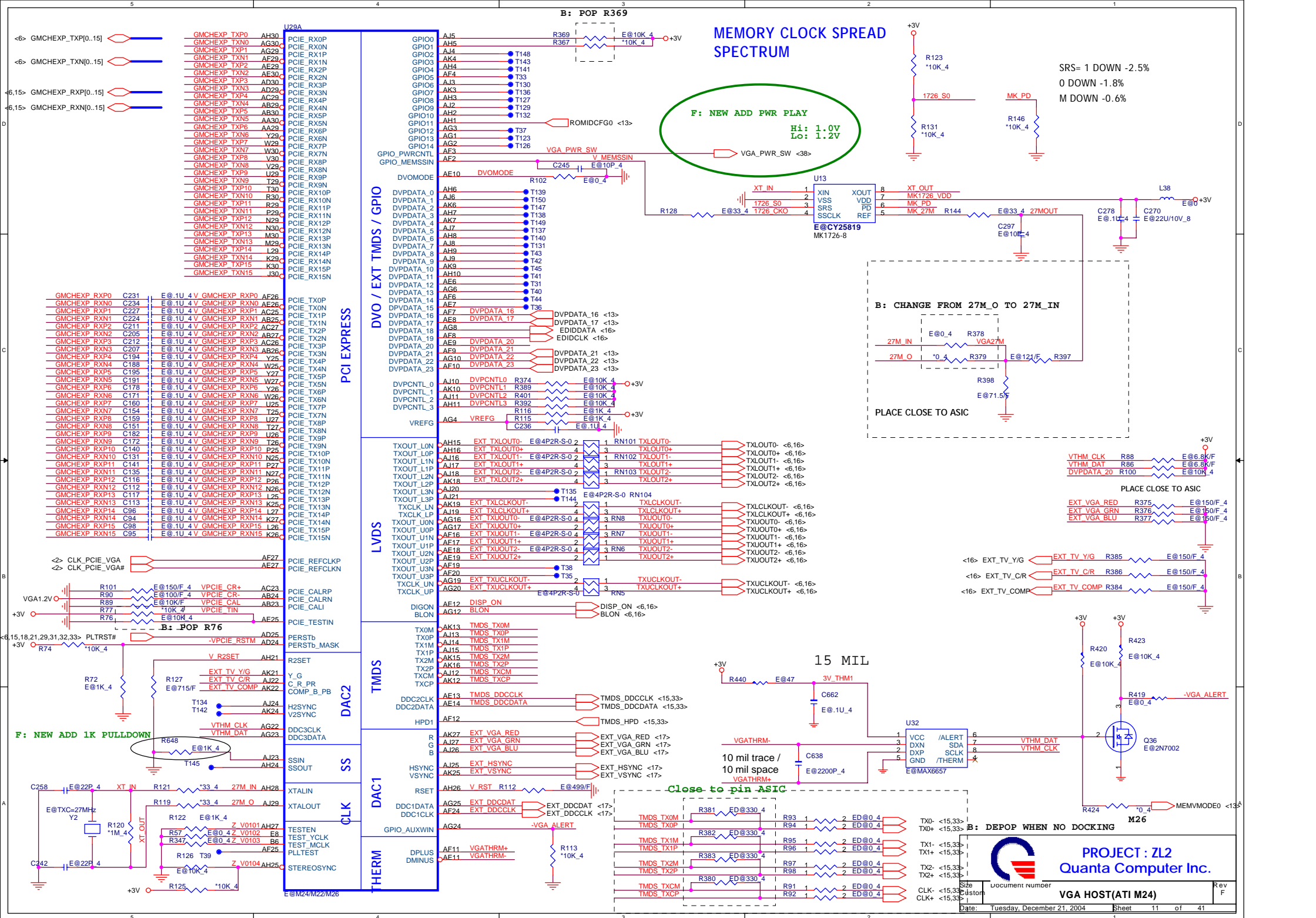


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# For terminal R-pack.





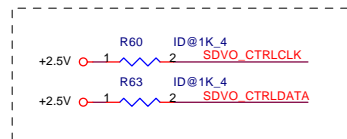






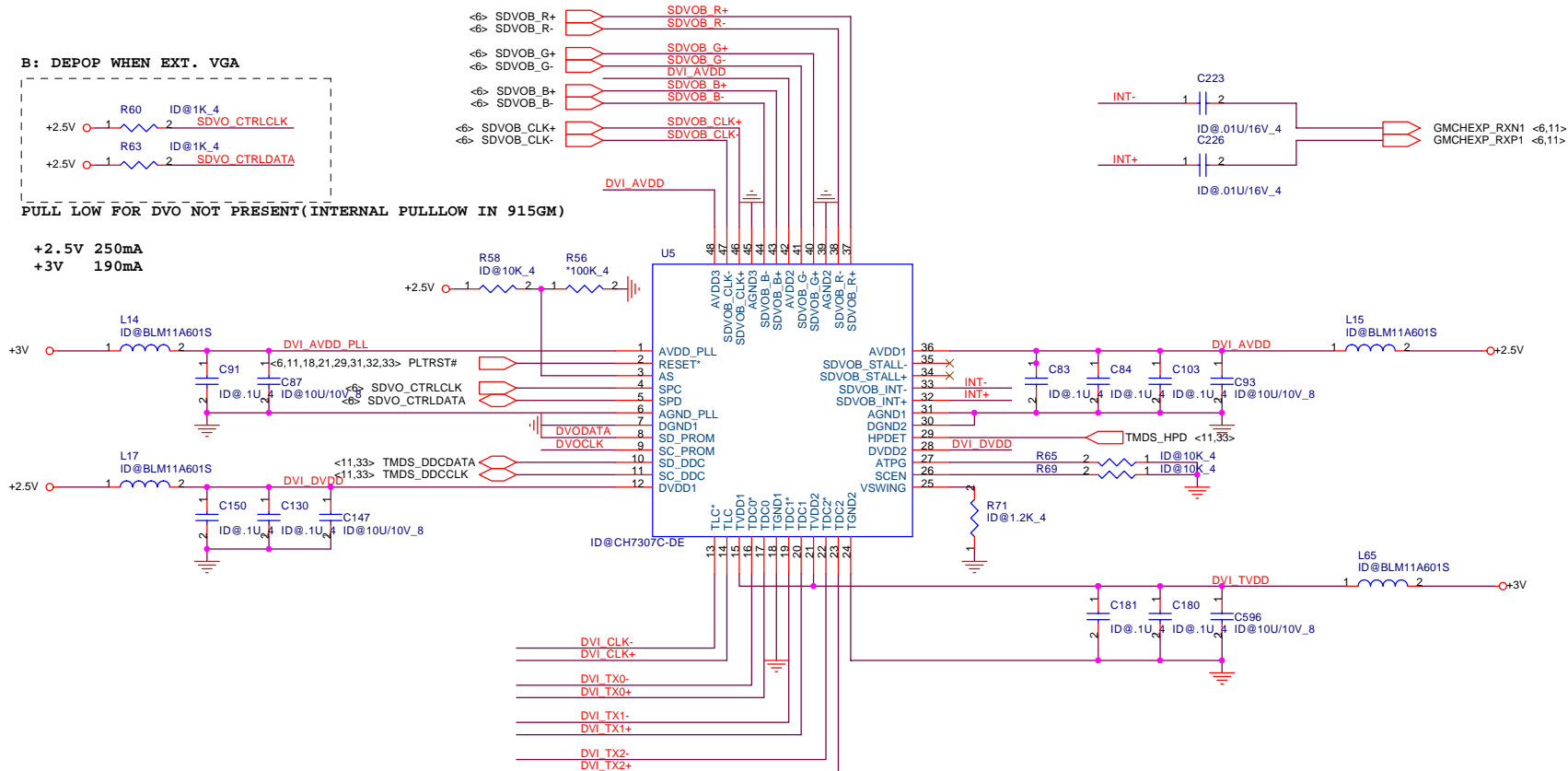


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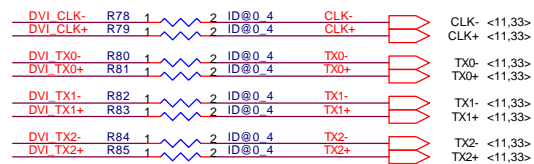
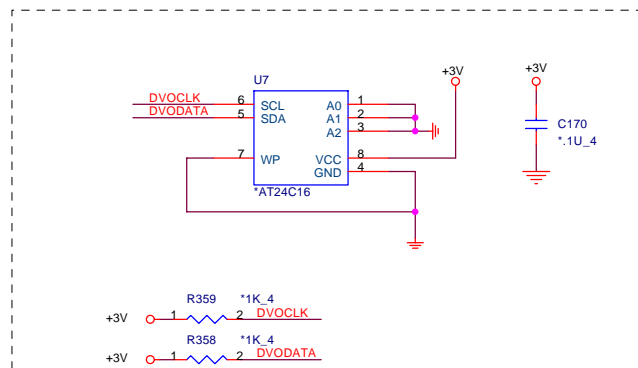


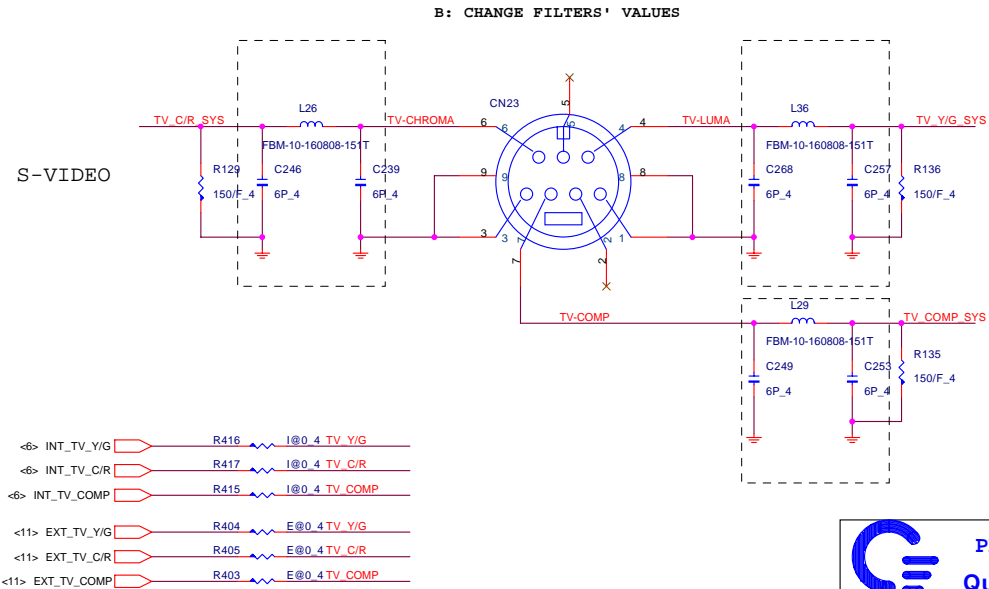
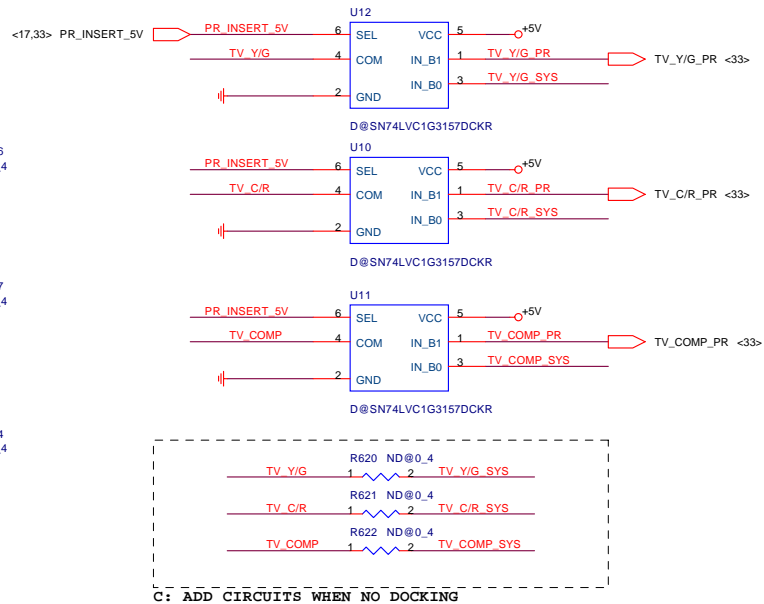
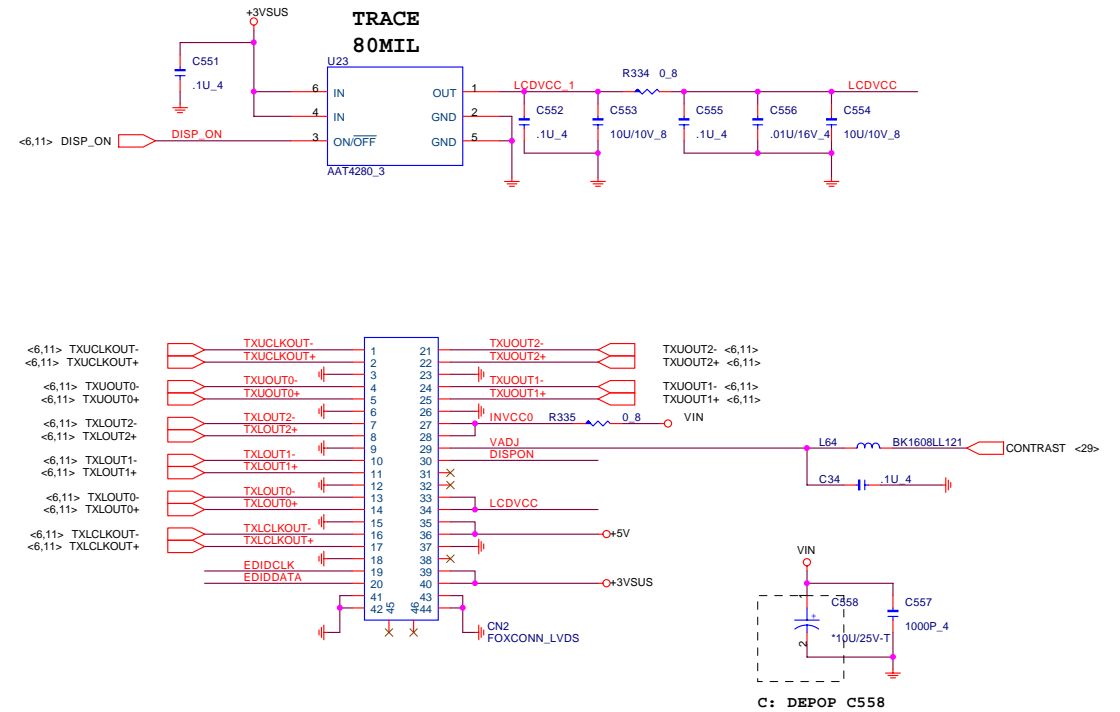
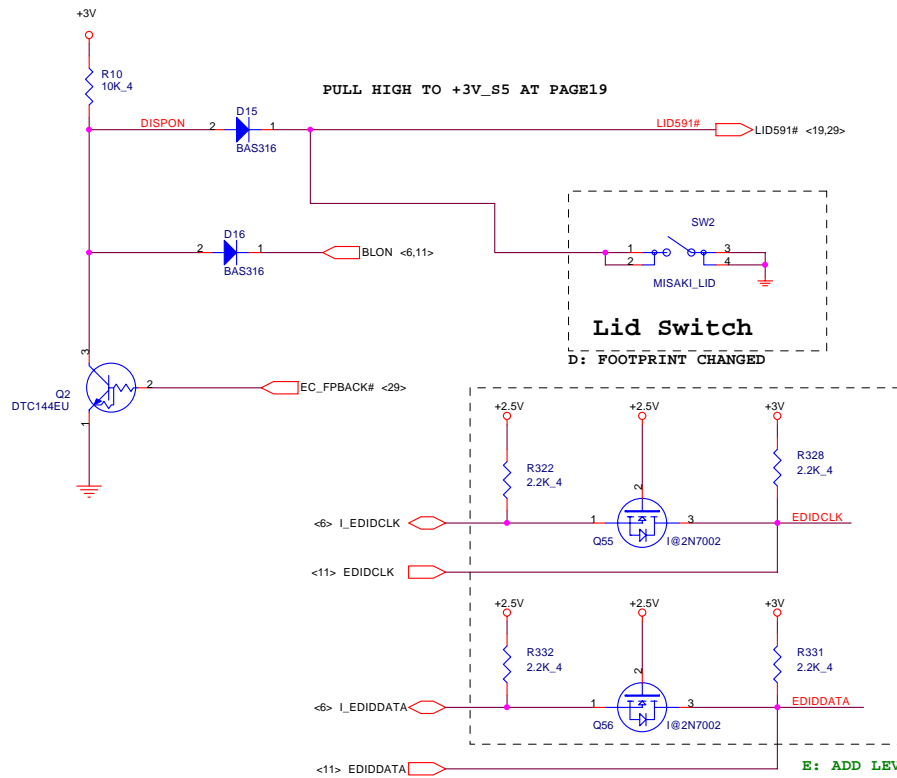
PULL LOW FOR DVO NOT PRESENT (INTERNAL PULLLOW IN 915GM)

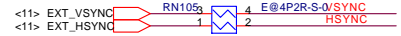
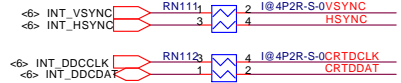
+2.5V 250mA  
+3V 190mA



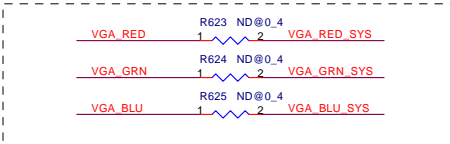
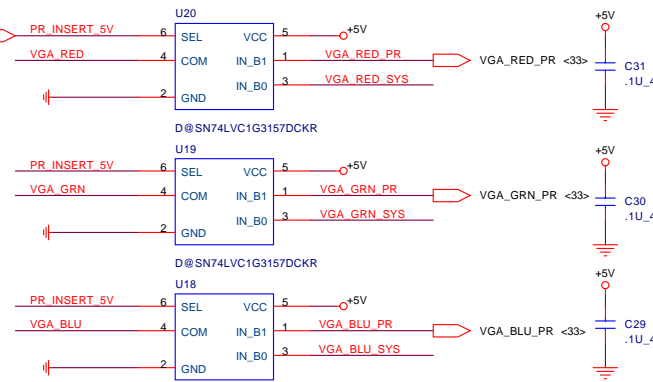
# B: ALWAYS NOT ON, TEST ONLY



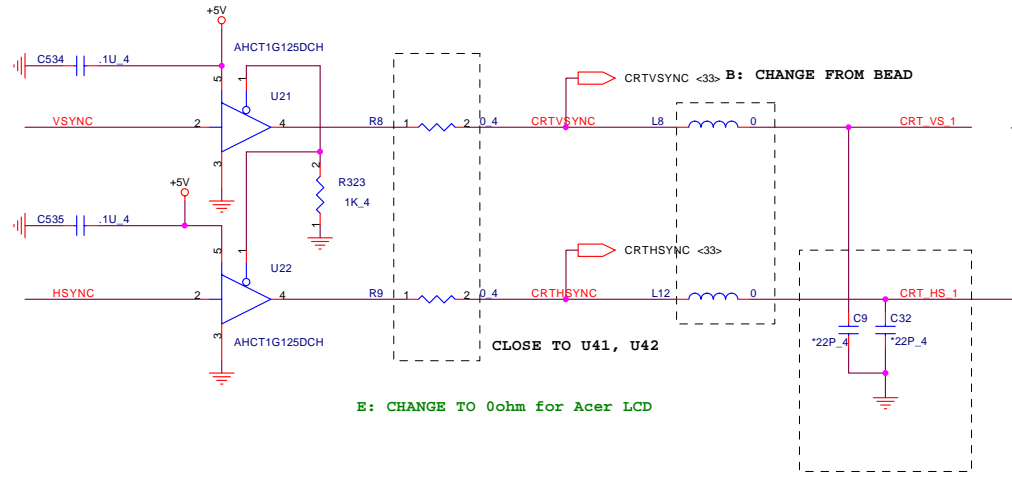
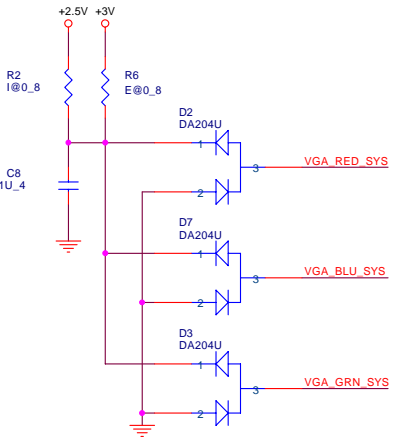
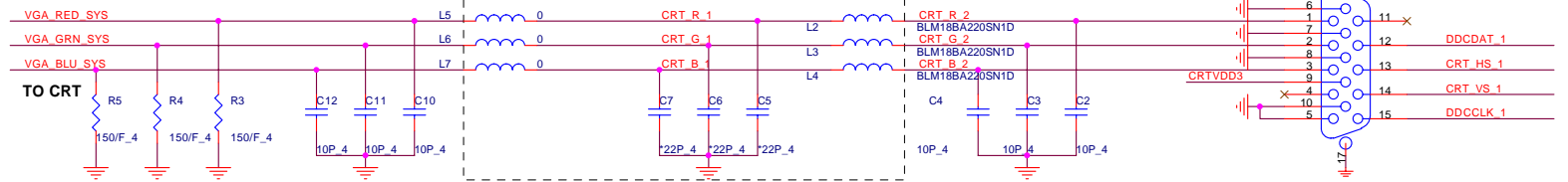




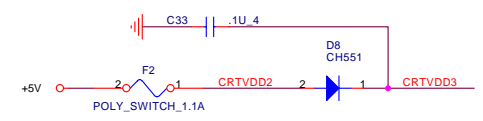
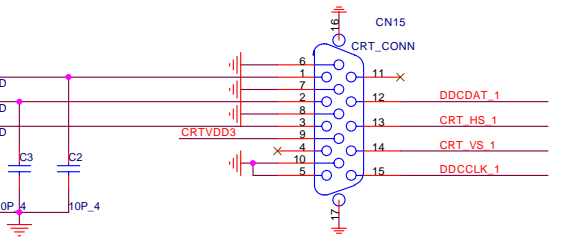
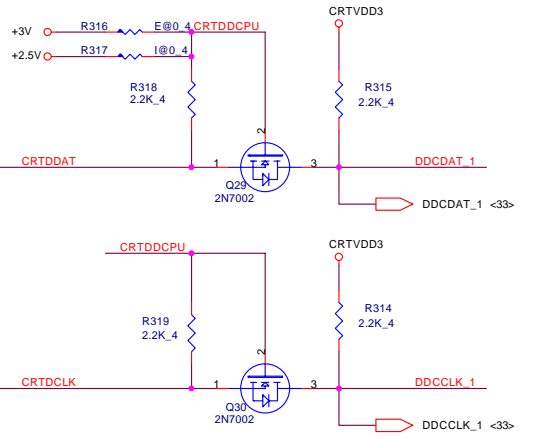
SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



C: ADD CIRCUITS WHEN NO DOCKING



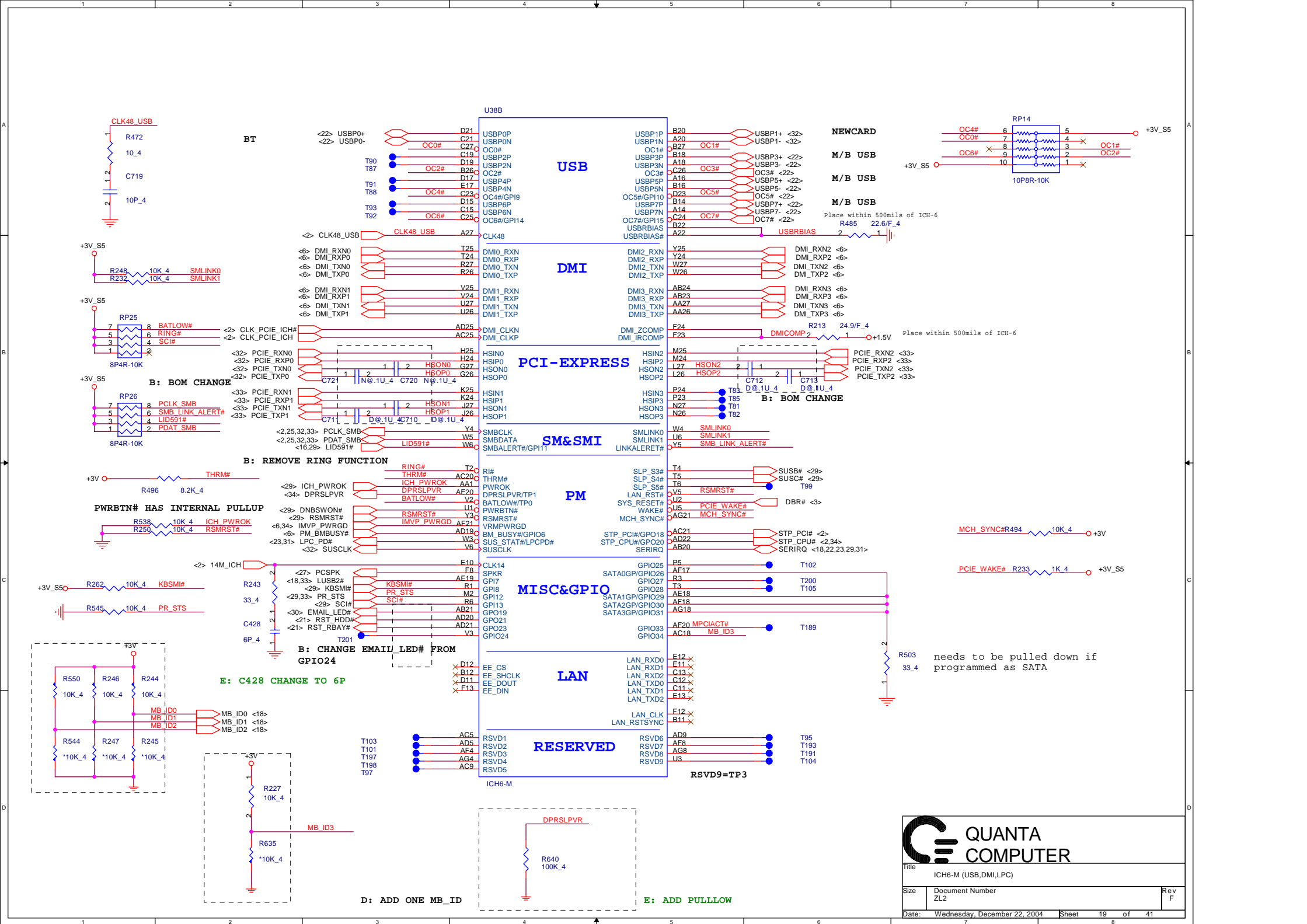
E: CHANGE TO 0ohm for Acer LCD



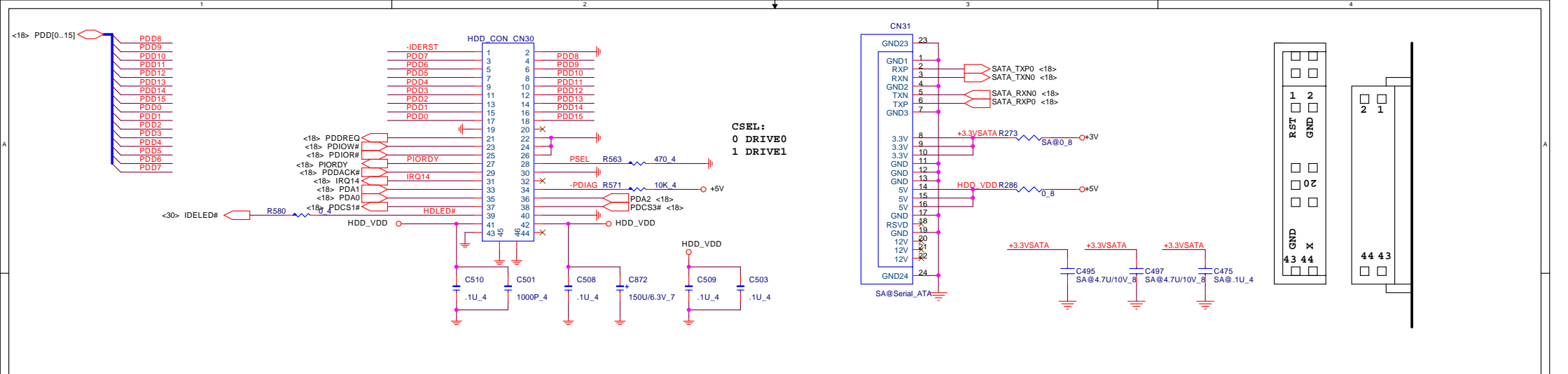
PROJECT : ZL2  
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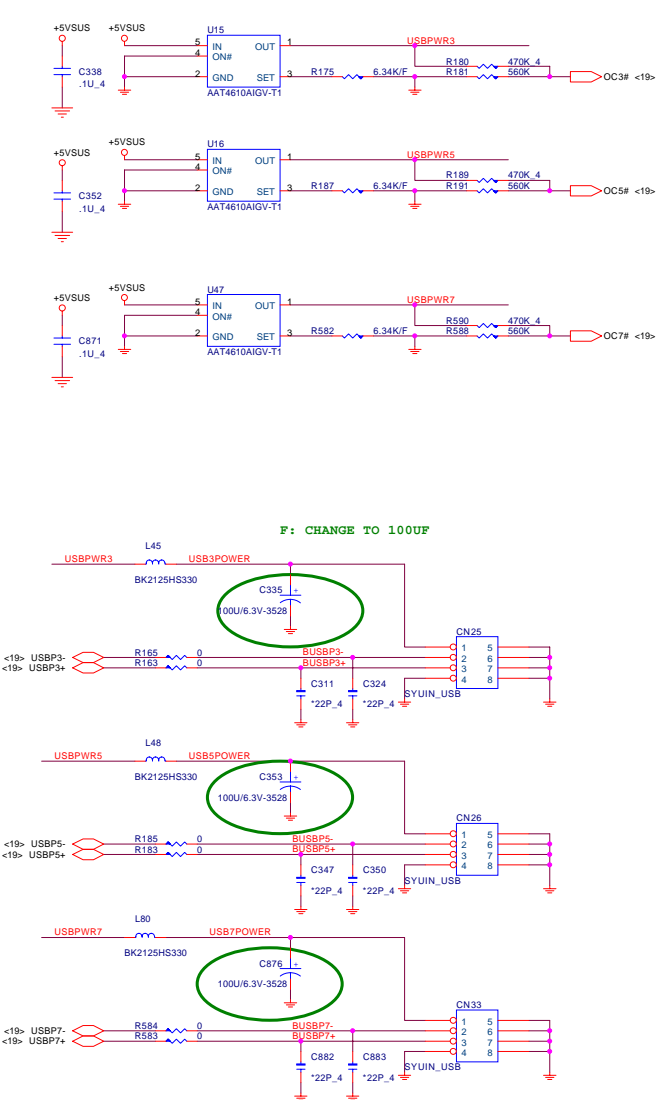
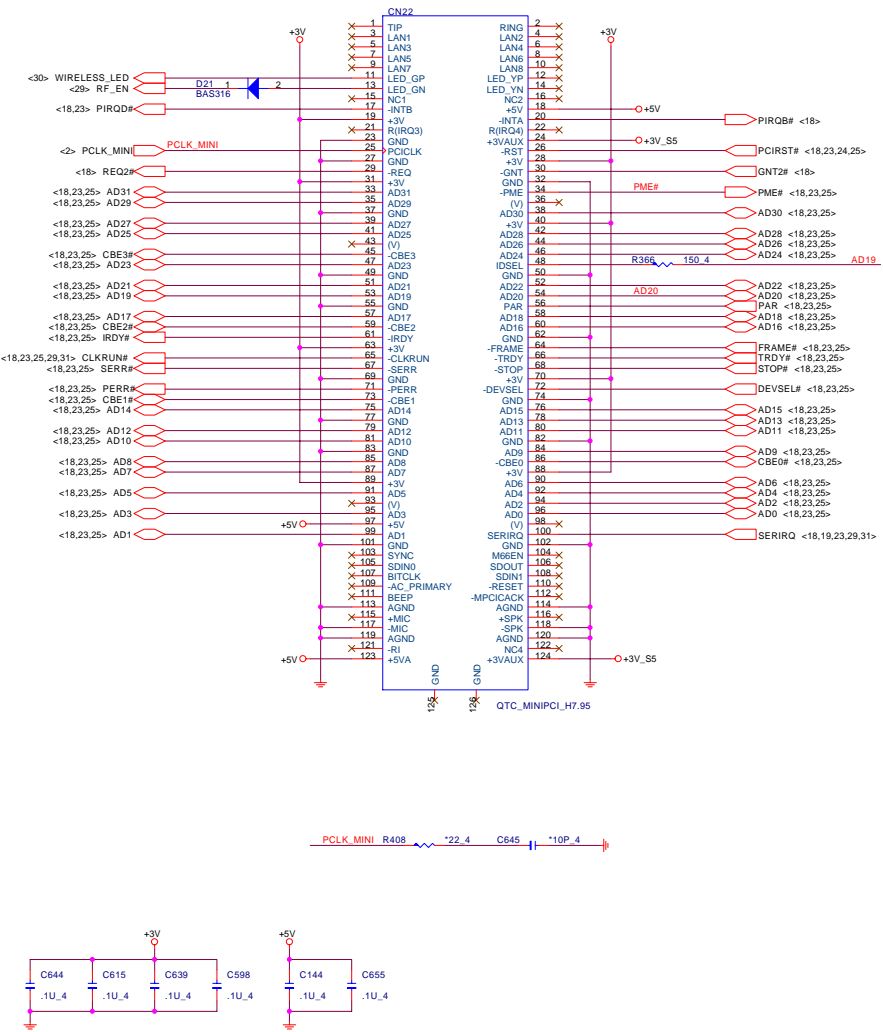




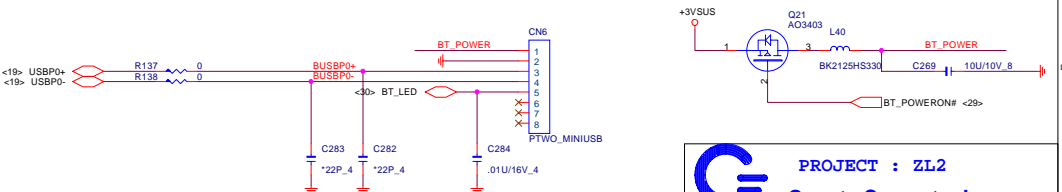


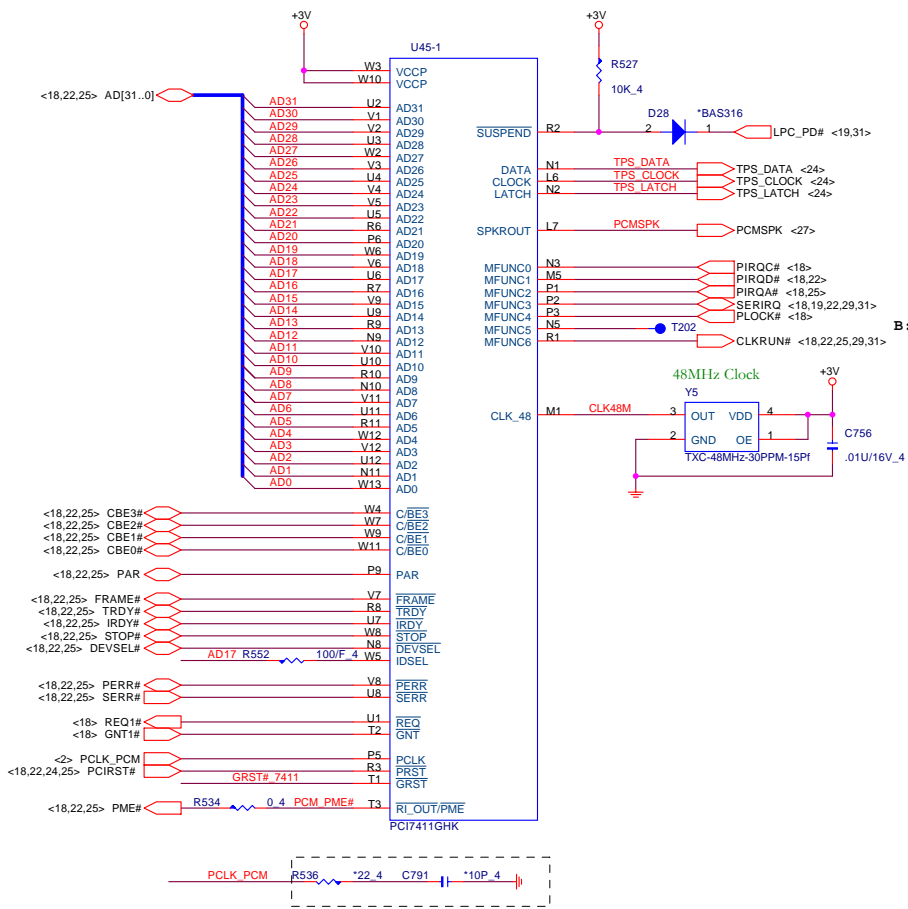
ID Select : AD20  
Interrupt Pin : INTB# , INTC#  
Request Indicate : REQ1#  
Grant Indicate : GNT1#

MINI-PCI



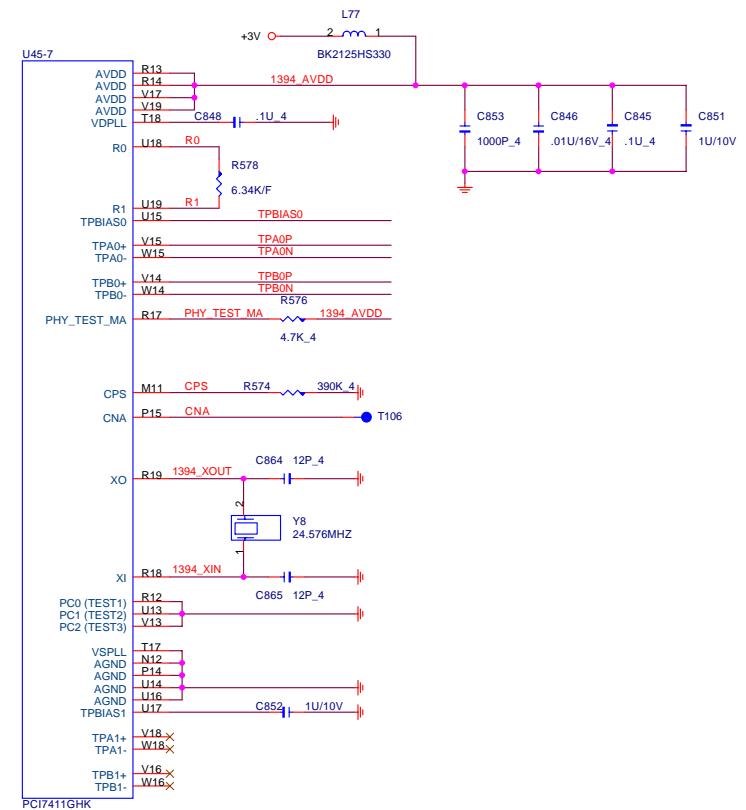
B: REMOVE CHOKE PADS





**B: REMOVE RING FUNCTION**

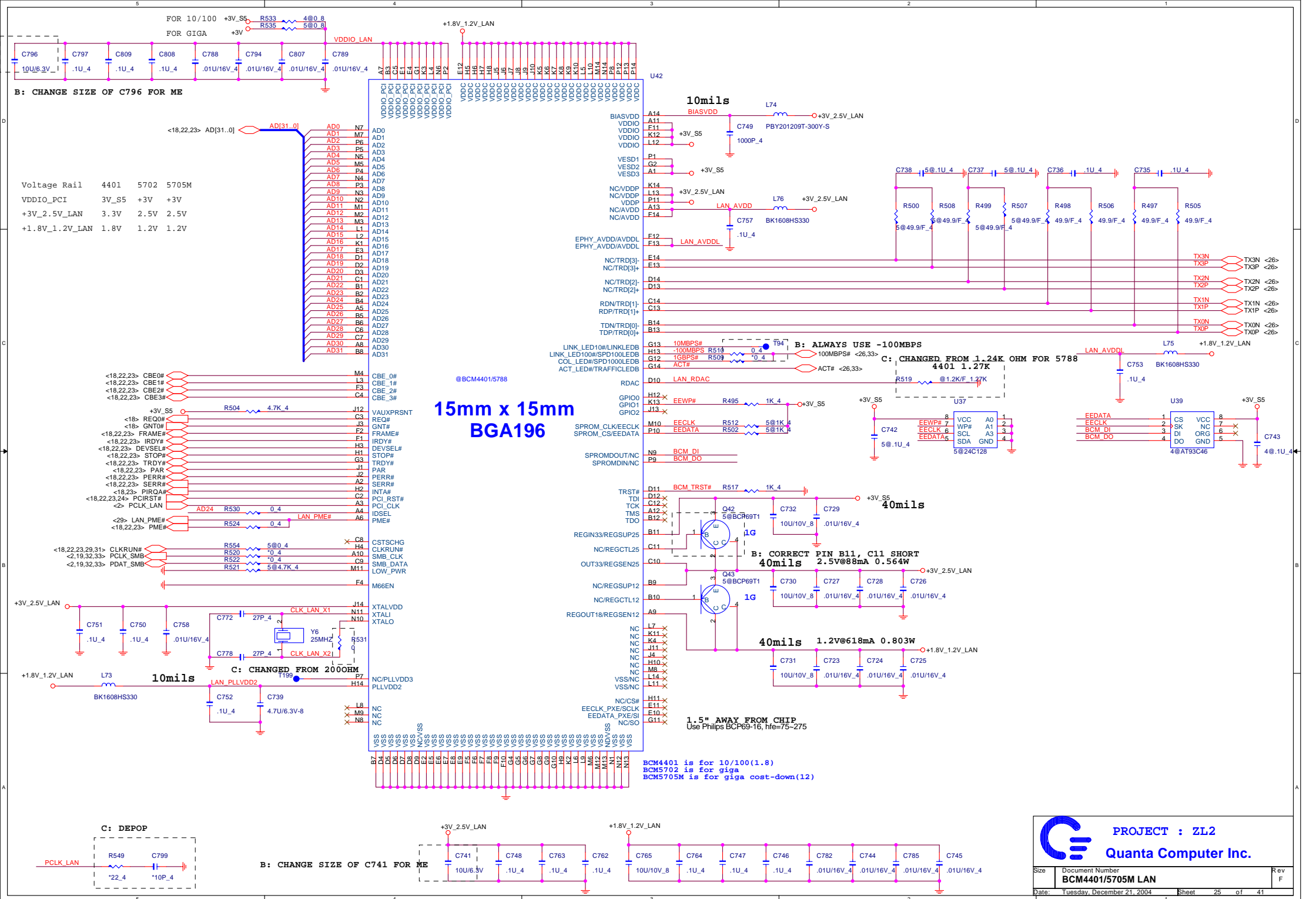
**E: ADD PULL-LOW**

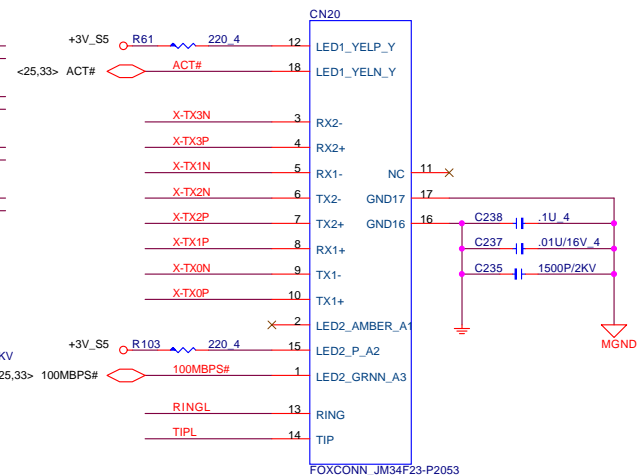
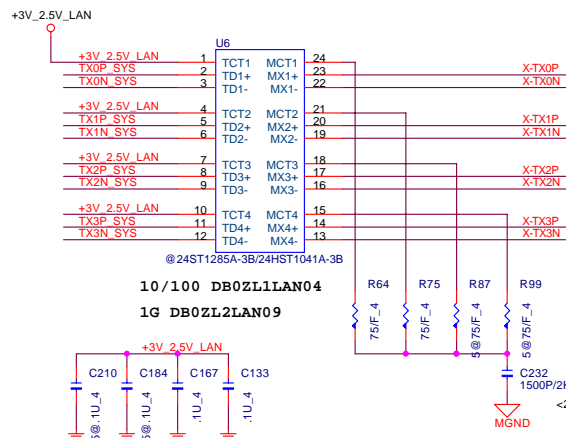
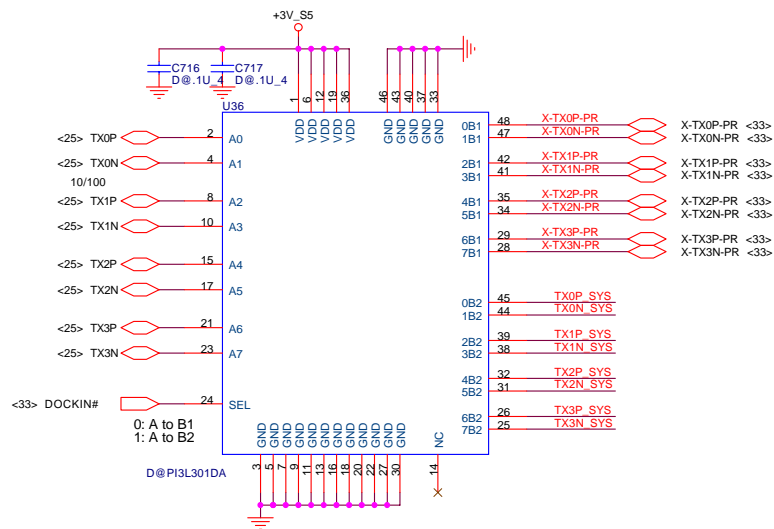


**B: REMOVE 1394CHOKE PADS**

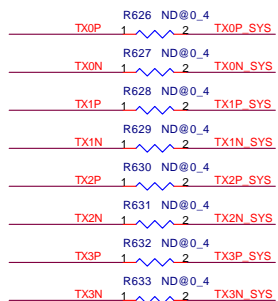






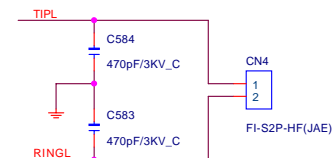


B: DEPOP C210, C184, R87, R99 WHEN NO 1G LAN



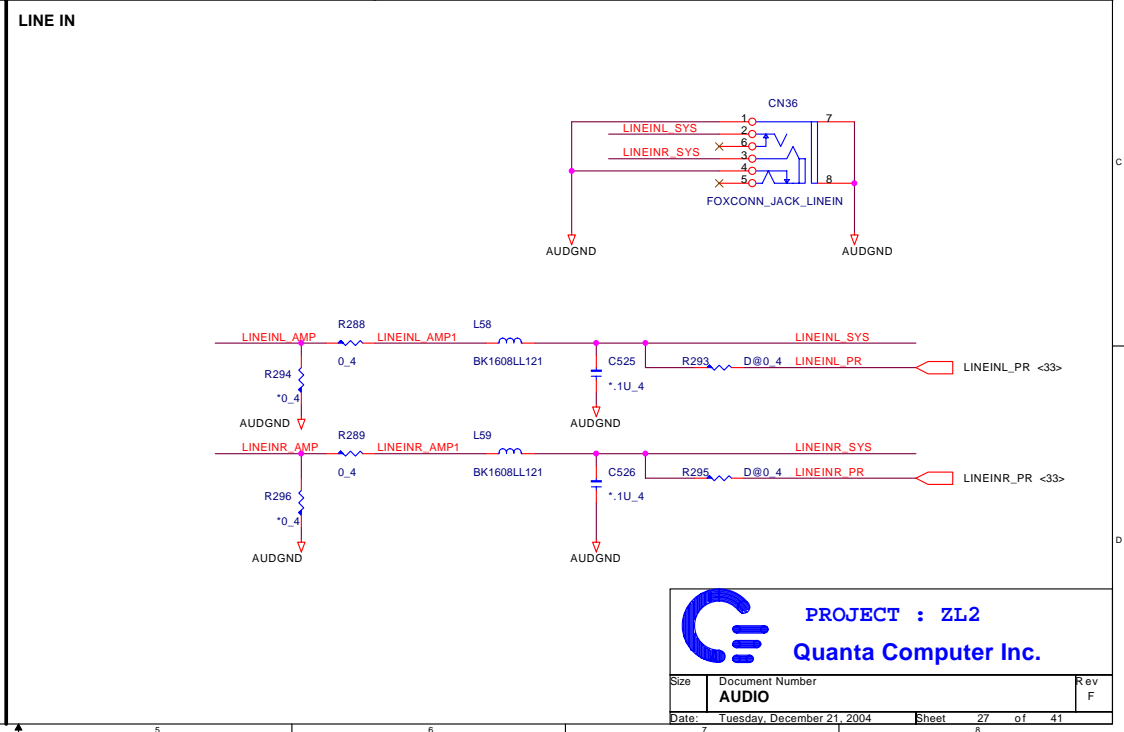
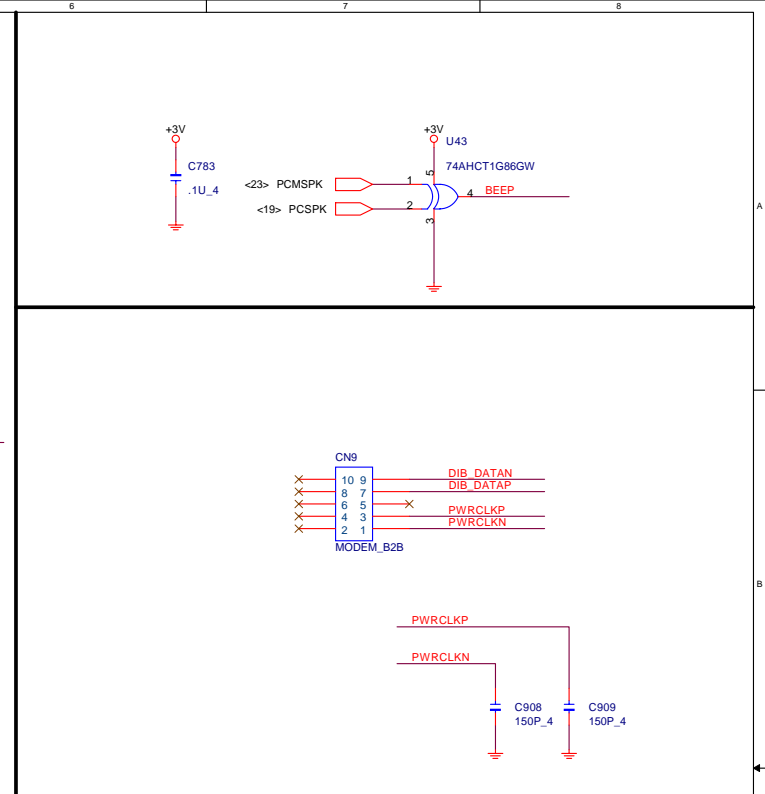
C: ADD CIRCUITS WHEN NO DOCKING

E: ADD FOR EMI REQUEST

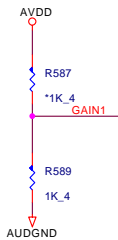


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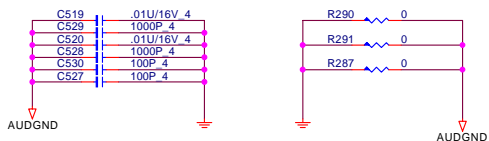
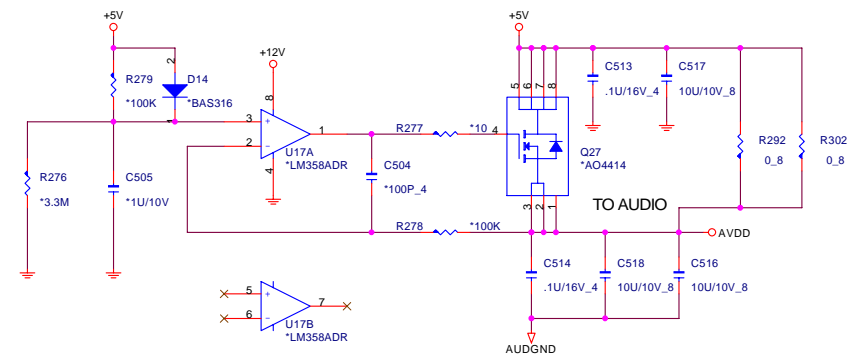
The AMC20463-004 modem is used for mother board family MBAMC20463-004.



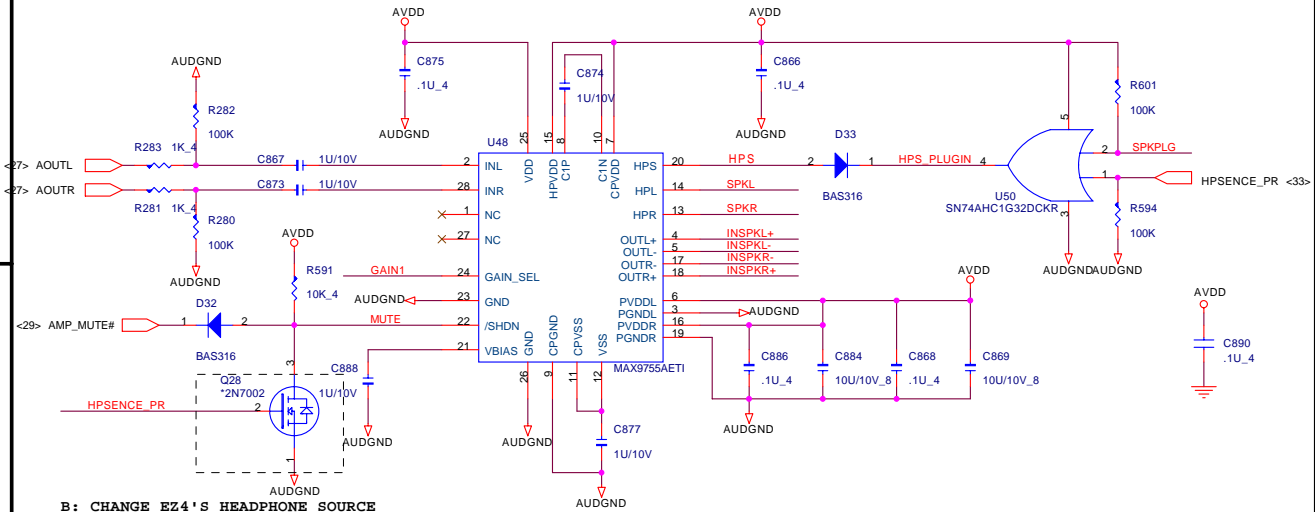
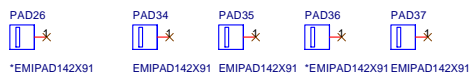
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



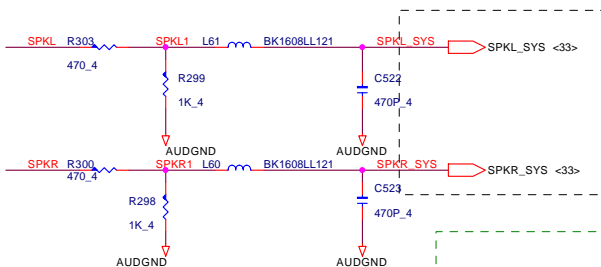
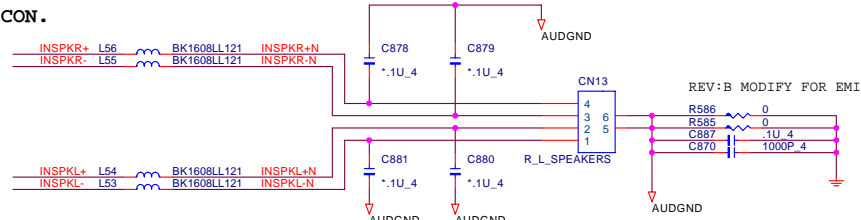
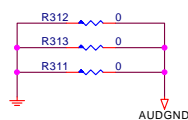
# AMP POWER



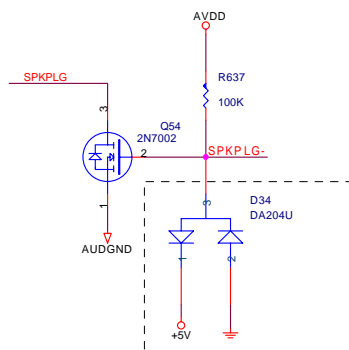
- F: RESTORE PAD26 FOR EMI REQUEST
- E: REMOVE PAD26, AND PAD35 CHANGE LOCATION
- B: ADD SPRING FOR MODEM CABLE



# SPEAKER CON.

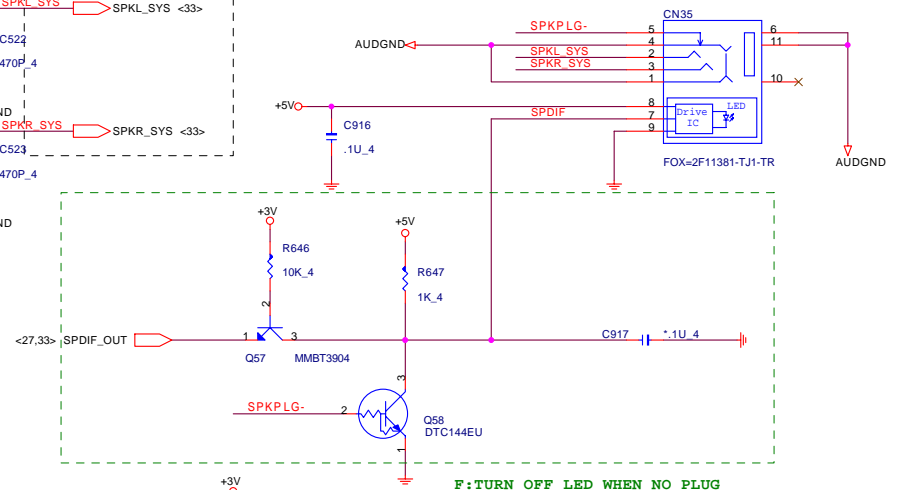


# B: CHANGE EZ4'S HEADPHONE SOURCE



# D: CHANGE TO SPDIF CONN

# LINE OUT&SPDIF

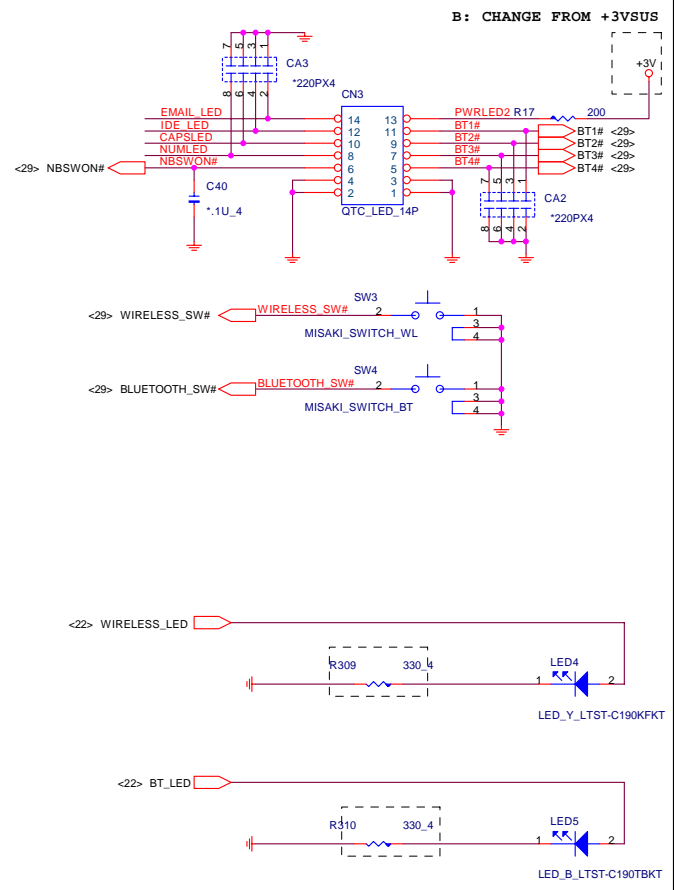
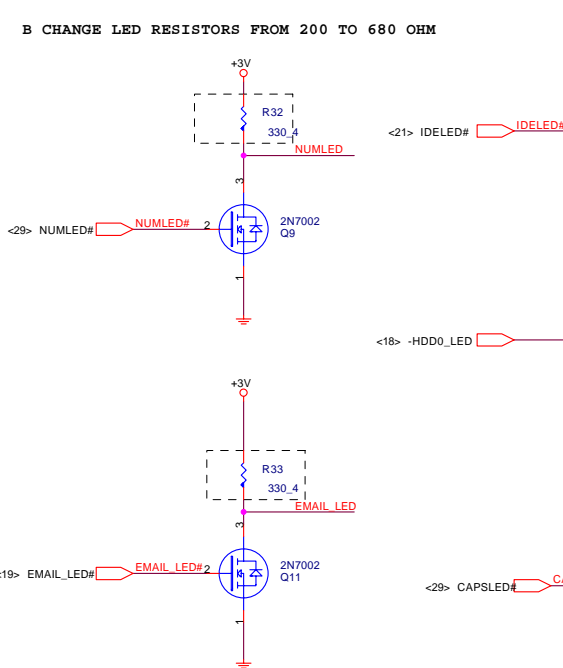
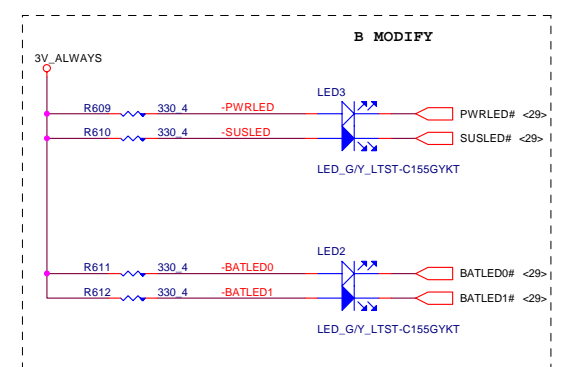
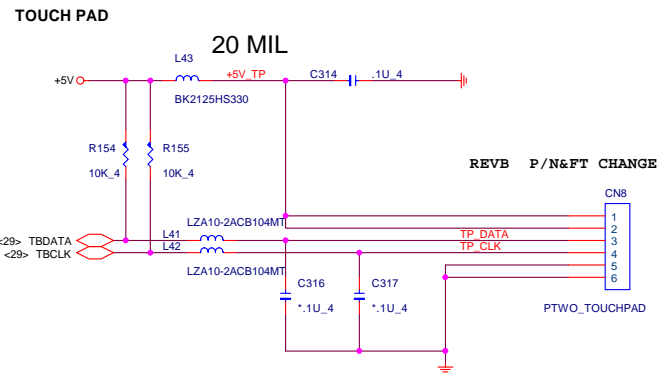
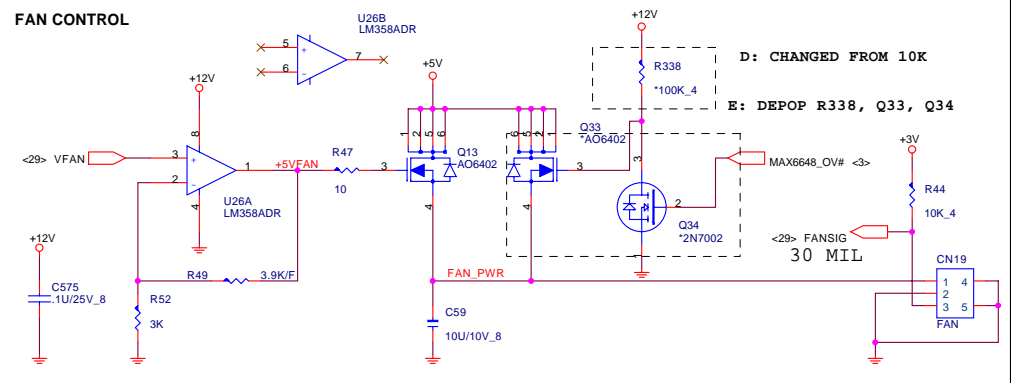
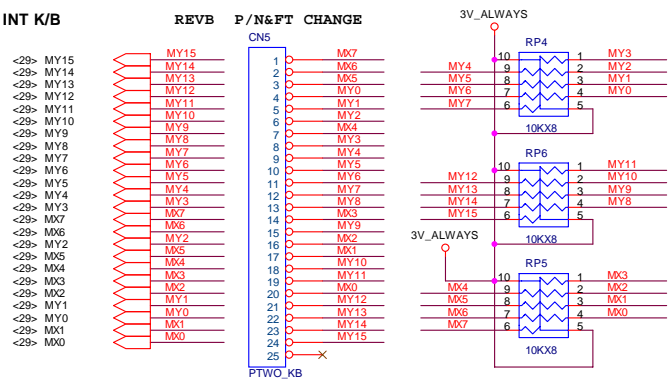


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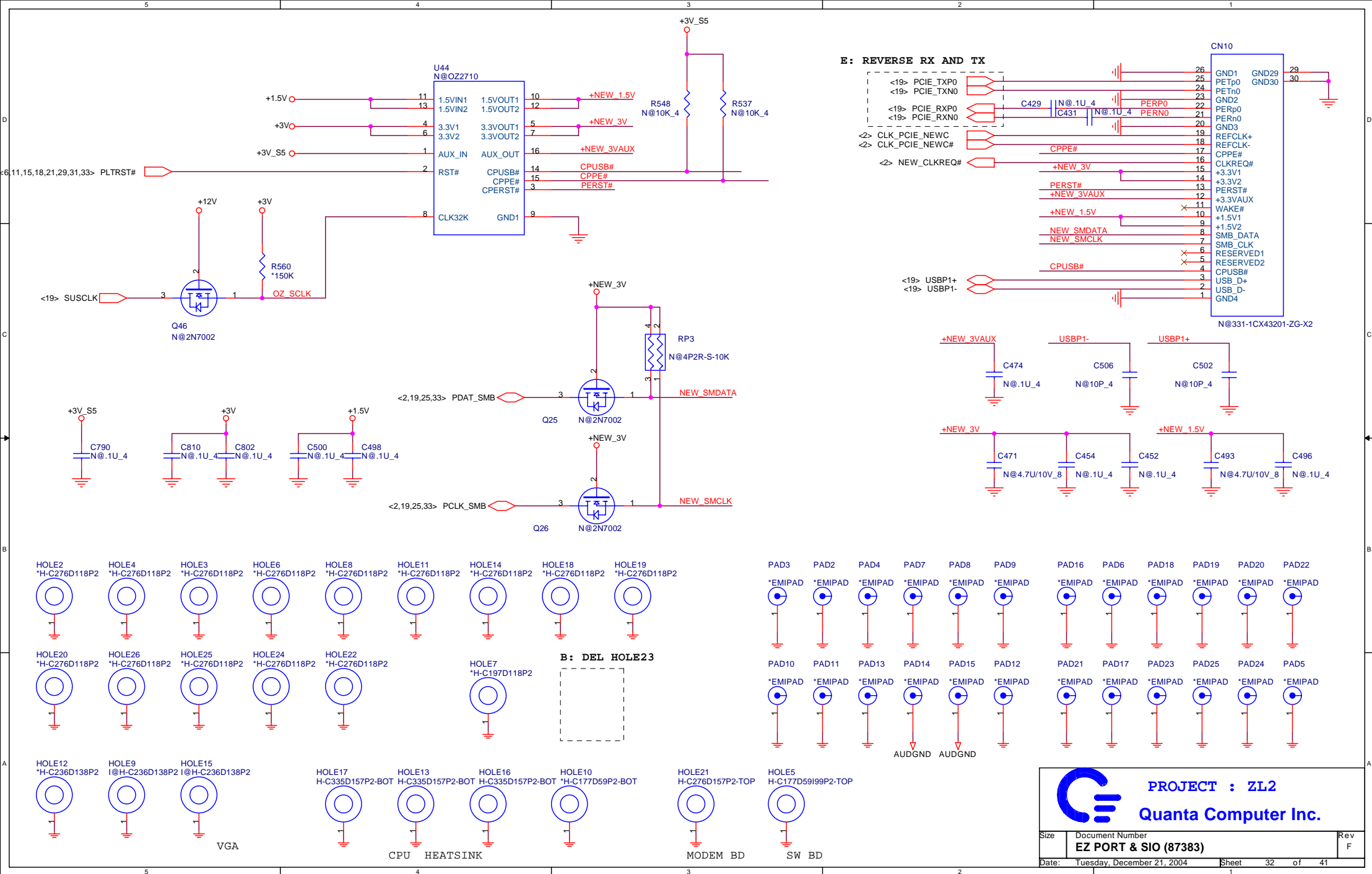
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	AUDIO AMP	F
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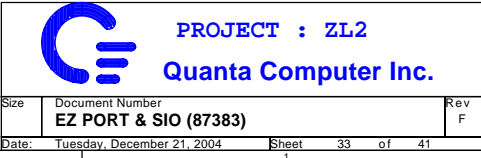
















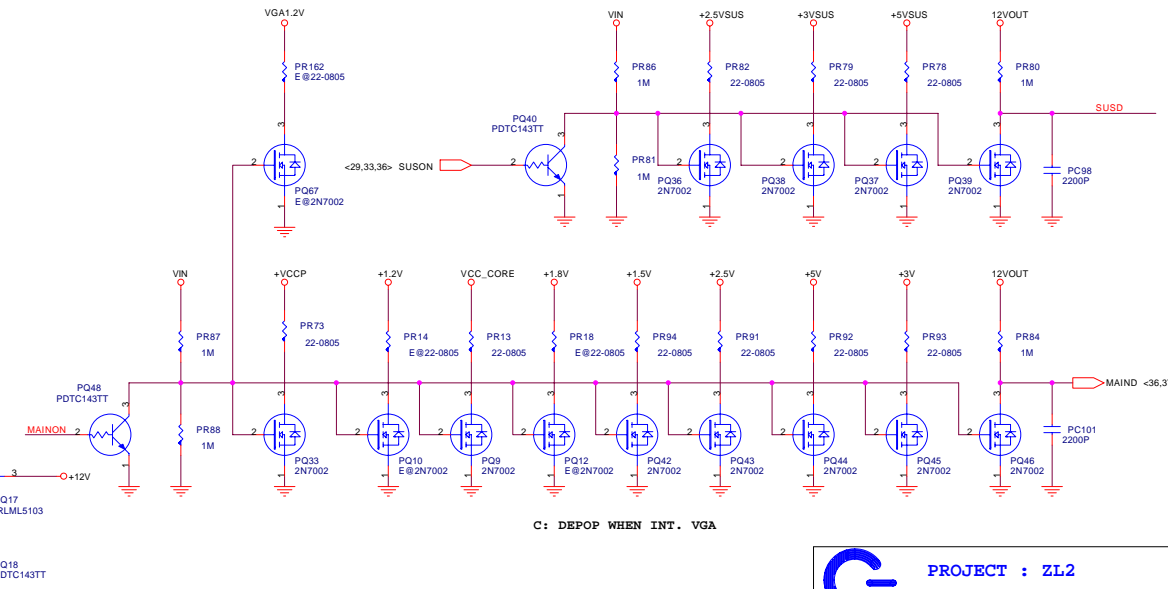
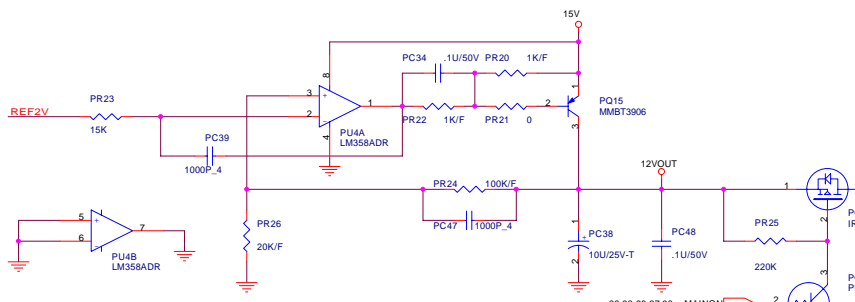
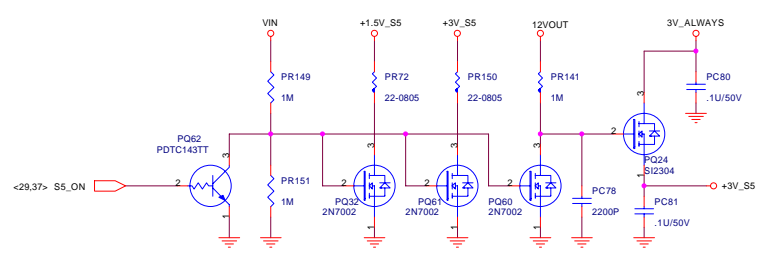
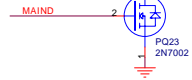
<3.30> MAX6648\_OV#

REF2V




<29.34,36,37,38> HWPG

MAIND



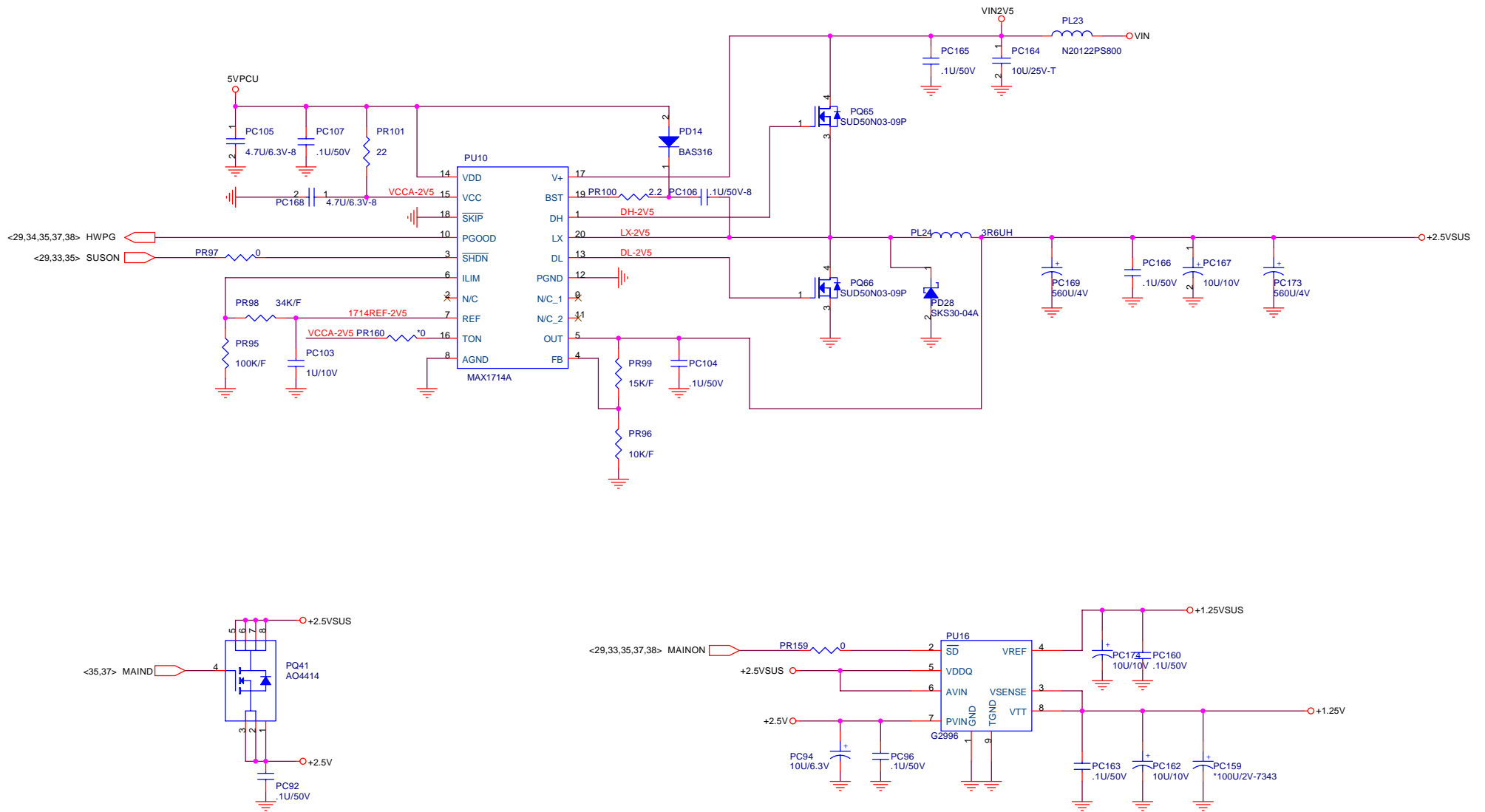
C: DEPOP WHEN INT. VGA



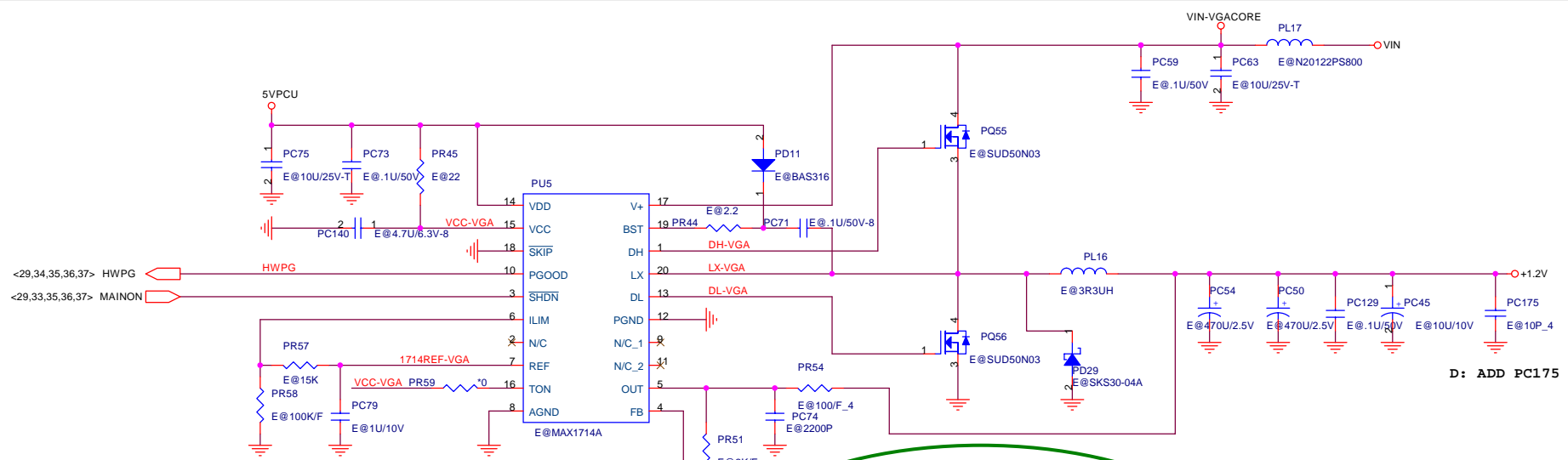
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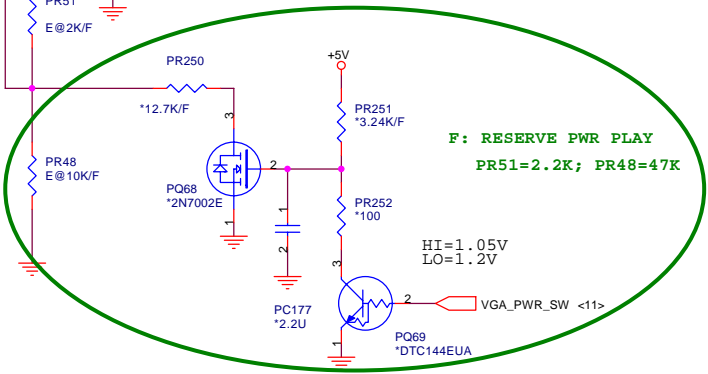
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D: ADD PC175



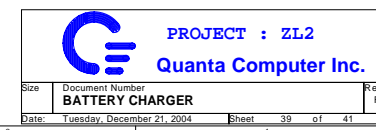
C: DEPOP WHEN INT. VGA



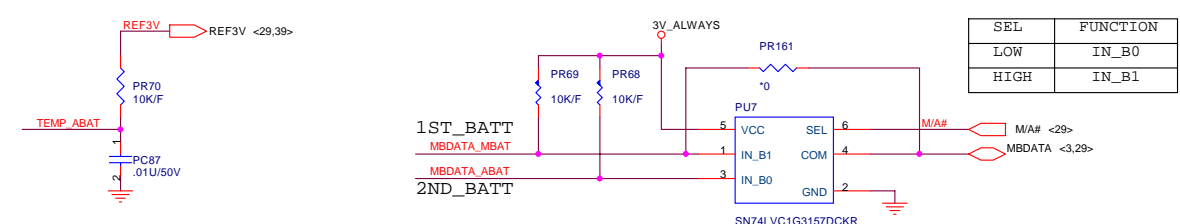
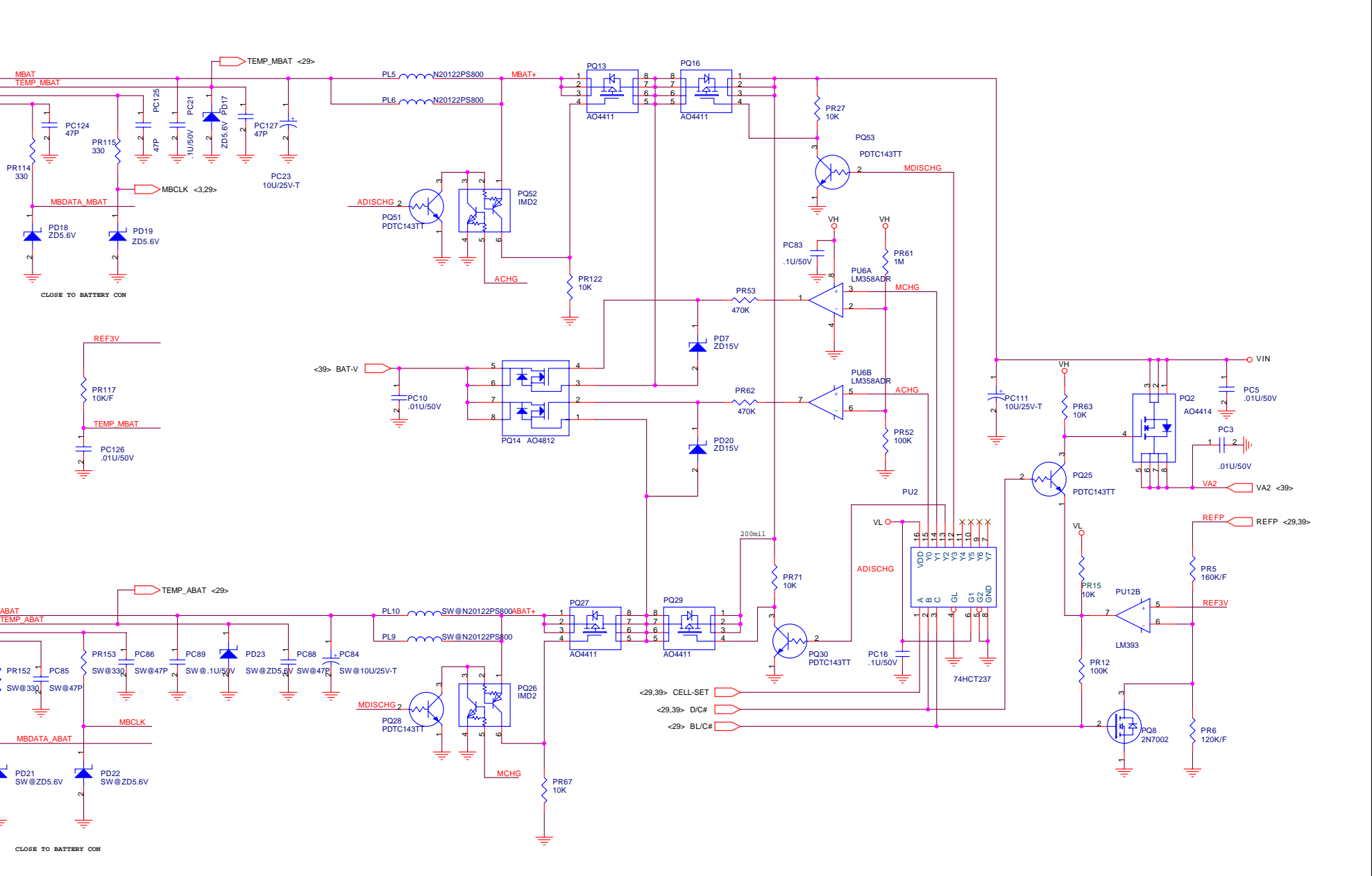
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SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



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MODEL:		REV:	CHANGE LIST:	PAGE	FROM	TO		
ZL2 MotherBoard	B	PAGE2	1. CHANGE FREQ. SETTINGS FOR DOTHANB 2. UNSTUFF SSC COMPONENTS 3. UNSTUFF COMPONENTS FOR DIFFERENT SKUS	1				
		PAGE3	1. REMOVE R449, PULL HIGH AT POWER SIDE 2. UNSTUFF ITP COMPONENTS 3. STUFF R441 FOR THEMTRIP#	2				
		PAGE4	1. STUFF R432, R433 FOR AUTO-SELECT	3				
		PAGE5	1. STUFF R162 FOR DOTHAN-B	4				
		PAGE6	1. STUFF R147, R148, R151, R143, R152, R159 ALWAYS 2. NOT STUFF DVO COMPONENTS WHEN NO DOCKING	5				
		PAGE8	1. NOT STUFF FILTER COMPONENTS WHEN EXT. VGA 2. CHANGE D10, D11 TO CH551					
		PAGE9	1. STUFF R76, PCIE TESTIN PULL LOW 2. STUFF R369, GPIO0 PULL HIGH 3. CHANGE CLK OUTPUT TO XTALIN 4. NOT STUFF DVI COMPONENTS WHEN NO DOCKING					
		PAGE12	1. ADD 220UF IN VGAI.2V					
		PAGE15	1. NOT STUFF R60, R63 WHEN NO DVO DEVICE 2. NOT STUFF DVO COMPONENTS WHEN NO DOCKING					
		PAGE16	1. CHANGE TV-OUT LC VALUES					
		PAGE18	1. ADD DAMPING ON LFRAME# FOR AUDIO NOISE 2. STUFF R469 AND UNSTUFF R467 FOR DOTHAN-B 3. NOT STUFF COMPONENTS FOR SATA WHEN NO SATA 4. NOT STUFF AC TERMINATION FOR PCLK_1CH					
		PAGE19	1. NOT STUFF PCIE COMPONENTS WHEN NO PCIE DEVICES 2. CHANGE EMAIL LED GPIO 3. CHANGE MB_ID SETTING					
		PAGE21	1. REMOVE R50 2. CHANGE Q15 FROM BJT TO 2N7002	7				
		PAGE23	1. REMOVE RING FUNCTION 1. REMOVE 1394 CHOKE PADS	8				
		PAGE24	1. CHANGE 3IN1 CONNECTOR	9				
		PAGE25	1. CHANGE LED CONNECTION FOR 1G LAN 2. CORRECT U42 PIN.B11 PIN.C11 SHORT 3. CHANGE C741 AND C792'S SIZE FOR ME	10				
		PAGE26	1. UNINSTALL C210, C184, R87, R99 WHEN 10-100	11				
		PAGE27	1. INSERT 4700P IN BEEP SIGNALS 2. REMOVE SPK_PR FROM CODEC 3. CHANGE C863 TO 10U 4. REVERSE MIC-SELECT 5. CHANGE R272 TO 0 OHM	12				
		PAGE28	1. CHANGE CONNECTION FOR SPKL-R TO EZ4 2. ADD SPRINGS FOR MODEM CABLE	13				
		PAGE29	1. CHANGE PR_INSERT# TO PR_ST5	14				
		PAGE30	1. CHANGE KB AND TP'S CONNECTOR 2. CHANGE LED CIRCUITS	15				
		PAGE31	1. REMOVE C889 2. STUFF AC TERMINATIONS FOR 14M_SIO	16				
		PAGE33	1. MODIFY EZ4 INTERFACE	17				
		PAGE 35	1. INCREASE CAPACITOR PC171 NEAR PR422. CHANGE COMPONENT PR38 SERIAL NUMBER FROM 0603 TO 1206 3. TAKE OFF PR39 PR43 PQ21 AND CHANGE NET NAME TO MAX6648_OV#	18				
		PAGE 35	3. INCREASE CAPACITOR C913 4. ADD DISCHARGE FOR VGAI.2V	19				
		PAGE 40	1. INCREASE RESISTOR PR161 NEAR PU7	20				
		PAGE 22	1. REMOVE CHOKE PADS	21				
		PAGE 39	1. TAKE OFF PQ50	22				
		PAGE 37	1. CHANGE PU8 NET NAME TO +2.5V	23				
		PAGE 17	1. CHANGE HSYNC& VSYNC'S BEADS TO 0 OHM	24				
		C		DA0ZL2MB8C3	25			
				PAGE2	1. ADD PULLUPS ON CLKREQ PINS	26		
				PAGE3	1. ADD THEMAL SHUTDOWN CIURCUITS	27		
				PAGE13	1. CHANGE OPTIONS TO HYNIX MEMORY	28		
				PAGE16	1. DEPOP C558	29		
PAGE17	1. CHANGE LC VALUES FOR RGB			30				
PAGE16, 17, 26	ADD 0 OHM RESISTORS TO SUBSTITUE SWITCHES WHEN NO DOCKING			31				
PAGE25	1. CHANGE R531 TO 0 OHM 1. CHANGE R519 TO 1.2K/F			32				
PAGE27	1. CHANGE C512, C511 TO .7UF 1. CHANGE R592 TO 100K			33				
PAGE28	1. DEPOP Q28			34				
PAGE29	1. CHANGE BATLED0,1# PINS TO IOPJ6,7			35				
PAGE24	1. CHANGE 3-IN-1 CONNECTOR							
PAGE 34	1. INCREASE CAPACITOR PC172 10U/25V IN VIN.1907A 2. INCREASE SCHOTTKY DIODE PD24 SKS30-04A IN MAX1907LX .							
PAGE 35	1. INCREASE ZENER DIODE PD25 ZD5.6V SERIES WITH VIN1999 AND PR249 2. CHANGE NET NAME TO 1999_CHT#							
PAGE 36	1. CHANGE MOSFET SUD50N03-09P TO PQ65 PQ66 2. INCREASE CAPACITOR PC173 560U/4V IN +2.5VSUS 3. INCREASE CAPACITOR PC174 10U/10V IN +1.25VSUS 4. INSERT PR250 BETWEEN PU16 PIN2 AND PIN 5 .							
PAGE 37	1. CHANGE MOSFET SUD50N03-09P TO PQ34 PQ47 AND PQ35 2. TAKE OFF JUMP 3.CHANGE CAPACITOR PC155 PC146 TO 560U/4V							
PAGE 38	1. CHANGE MOSFET SUD50N03-09P TO PQ5 PQ6 PQ55 AND PQ56 2. TAKE OFF JUMP 3. CHANGE CHOKE PL16 TO 3R3UH 4.CHANGE CAPACITOR PC27 TO E@470U/2.5V							
PAGE 39	1. EXCHANGE NET NAME 3V_ALWAYS AND CELL-SET							
PAGE 23,25,29,31	1. DEPOP RC FILTERS ON PCI CLOCKS							
D		PAGE2	1. DEPOP R615 2. ADD 47P ON 14M_SIO	28				
		PAGE3	1. POP R449	29				
		PAGE6	1. DEPOP RP7	30				
		PAGE16	1. LID SW FOOTPRINT CHANGED	31				
		PAGE19	1. ADD MB_ID3	32				
		PAGE21	1. CHANGE C82's RATING TO 25V	33				
		PAGE28	1. PHONE JACK CHANGED TO SPDIF	34				
		PAGE30	1. CHANGE R338 TO 100K	35				
		PAGE31	1. CHANGE R28 AND C41's VALUE					
		PAGE33	1. ADD EMI CAPS					
		PAGE34	1. ADD CAP PC176					
		PAGE35	1. CHANGE CAP PC69 COMPONENT 2. CHANGE CAP PC72 CONNECT POINT 3. CHANGE RESISTOR PR147 COMPONENT					
		E		PAGE2	Change R200, R202, C915 value to pass EA	PAGE18	Change GPIO pin define, add R645	
				PAGE3,30	DEPOP COMPONENTS FOR MAX6648_OV#	PAGE20	D25 change to CH551	
				PAGE6	1. DEPOP R186, R184	PAGE31	Change C41 to 6pF	
PAGE8	Add C918 to solve TV issue			PAGE33	Modify EZ4 pin define, add R641, 642, 644 for EMI			
PAGE12	Add C919,920, change L25,27,67							
PAGE16	1. ADD LEVEL SHIFT FOR EDID							
PAGE16	Change R8, R9, remove C9, C32 for pass Acer LCD							
PAGE19	ADD 100K PULLLOW ON DPRSLPVR, change C428							
PAGE21	1. CHANGE SWAP-ODD RESET			PAGE 35	1. CHANGE PC68 COMPONENT.			
PAGE23	1. ADD PULL-LOW ON PCMSPK			PAGE 36	1. CHANGE PR99 PD28 COMPONENT.			
PAGE24	1. ADD 330HM CURRENT LIMIT ON VCC_XD			PAGE 37	1. CHANGE PD26 PD27 COMPONENT.			
PAGE32	1. REVERSE RX AND TX			PAGE 38	1. CHANGE PR51 COMPONENT.			
				PAGE 39	1. CHANGE PQ7 COMPONENT.			
					2. CHANGE PR74 PR77 PU8 PC147 PC150 COMPONENT.			
					2. CHANGE PD29 PD30 COMPONENT.			
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	CHANGE LIST		F					
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PROJECT : ZL2				APPROVE BY: SELMON LIU		DRAWING BY:JOE LIN		
MB ASSY'S P/N : 31ZL1MB0004				PROJECT LEADER: SELMON LIU		DOCUMENT NO:		
				REV		DATE :2004/06/01		